



PIC18 CONFIGURATION SETTINGS ADDENDUM

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable."

Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE. Microchip disclaims all liability arising from this information and its use. Use of Microchip's products as critical components in life support systems is not authorized except with express written approval by Microchip. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights.

Trademarks

The Microchip name and logo, the Microchip logo, Accuron, dsPIC, KEELOQ, microID, MPLAB, PIC, PICmicro, PICSTART, PRO MATE, PowerSmart, rfPIC, and SmartShunt are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

AmpLab, FilterLab, Migratable Memory, MXDEV, MXLAB, PICMASTER, SEEVAL, SmartSensor and The Embedded Control Solutions Company are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Analog-for-the-Digital Age, Application Maestro, dsPICDEM, dsPICDEM.net, dsPICworks, ECAN, ECONOMONITOR, FanSense, FlexROM, fuzzyLAB, In-Circuit Serial Programming, ICSP, ICEPIC, Linear Active Thermistor, MPASM, MPLIB, MPLINK, MPSIM, PICKit, PICDEM, PICDEM.net, PICLAB, PICTail, PowerCal, PowerInfo, PowerMate, PowerTool, rLAB, rfPICDEM, Select Mode, Smart Serial, SmartTel, Total Endurance and WiperLock are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

All other trademarks mentioned herein are property of their respective companies.

© 2005, Microchip Technology Incorporated, Printed in the U.S.A., All Rights Reserved.

 Printed on recycled paper.

QUALITY MANAGEMENT SYSTEM
CERTIFIED BY DNV
== ISO/TS 16949:2002 ==

Microchip received ISO/TS-16949:2002 quality system certification for its worldwide headquarters, design and wafer fabrication facilities in Chandler and Tempe, Arizona and Mountain View, California in October 2003. The Company's quality system processes and procedures are for its PICmicro® 8-bit MCUs, KEELOQ® code hopping devices, Serial EEPROMs, microperipherals, nonvolatile memory and analog products. In addition, Microchip's quality system for the design and manufacture of development systems is ISO 9001:2000 certified.

Table of Contents

PIC18C242	1
PIC18C252	2
PIC18C442	3
PIC18C452	4
PIC18C601	5
PIC18C658	6
PIC18C801	7
PIC18C858	8
PIC18F1220	9
PIC18F1230	11
PIC18F1231	14
PIC18F1320	17
PIC18F1330	20
PIC18F1331	23
PIC18F2220	26
PIC18F2221	29
PIC18F2320	32
PIC18F2321	35
PIC18F2331	38
PIC18F2410	41
PIC18F242	44
PIC18F2420	46
PIC18F2431	49
PIC18F2439	52
PIC18F2450	54
PIC18F2455	58
PIC18F248	62
PIC18F2480	64
PIC18F24J10	67
PIC18F2510	68
PIC18F2515	71
PIC18F252	74
PIC18F2520	77

PIC18 Configuration Settings Addendum

PIC18F2525	80
PIC18F2539	83
PIC18F2550	85
PIC18F258	89
PIC18F2580	92
PIC18F2585	95
PIC18F25J10	98
PIC18F2610	100
PIC18F2620	103
PIC18F2680	106
PIC18F4220	110
PIC18F4221	113
PIC18F4320	116
PIC18F4321	119
PIC18F4331	122
PIC18F4410	126
PIC18F442	128
PIC18F4420	131
PIC18F4431	134
PIC18F4439	137
PIC18F4450	139
PIC18F4455	143
PIC18F448	147
PIC18F4480	149
PIC18F44J10	152
PIC18F4510	153
PIC18F4515	156
PIC18F452	159
PIC18F4520	162
PIC18F4525	165
PIC18F4539	168
PIC18F4550	170
PIC18F458	174
PIC18F4580	177
PIC18F4585	180
PIC18F45J10	183
PIC18F4610	185
PIC18F4620	188
PIC18F4680	191

PIC18F6310	195
PIC18F6390	197
PIC18F6410	199
PIC18F6490	201
PIC18F64J15	203
PIC18F6520	204
PIC18F6525	207
PIC18F6527	210
PIC18F6585	213
PIC18F65J10	216
PIC18F65J15	217
PIC18F6620	218
PIC18F6621	221
PIC18F6622	224
PIC18F6627	227
PIC18F6680	231
PIC18F66J10	234
PIC18F66J15	235
PIC18F66J60	236
PIC18F66J65	238
PIC18F6720	239
PIC18F6722	243
PIC18F67J10	247
PIC18F67J60	248
PIC18F8310	250
PIC18F8390	252
PIC18F8410	254
PIC18F8490	256
PIC18F84J15	258
PIC18F8520	260
PIC18F8525	263
PIC18F8527	266
PIC18F8585	269
PIC18F85J10	272
PIC18F85J15	274
PIC18F8620	276
PIC18F8621	279
PIC18F8622	282
PIC18F8627	286

PIC18 Configuration Settings Addendum

PIC18F8680	290
PIC18F86J10	293
PIC18F86J15	295
PIC18F86J60	297
PIC18F86J65	298
PIC18F8720	300
PIC18F8722	303
PIC18F87J10	308
PIC18F87J60	310
PIC18F96J60	311
PIC18F96J65	313
PIC18F97J60	315
PIC18LF2423	317
PIC18LF2523	320
PIC18LF4423	324
PIC18LF4523	327

Configuration Settings

This addendum lists the configuration settings available for each of the PIC18 devices for use with MPLAB® C18's #pragma config directive and MPASM™ assembler's CONFIG directive.

PIC18C242

Code Protect:

CP = ON	Enabled
CP = OFF	Disabled

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18C252

Code Protect:

CP = ON	Enabled
CP = OFF	Disabled

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18C442

Code Protect:

CP = ON	Enabled
CP = OFF	Disabled

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18C452

Code Protect:

CP = ON	Enabled
CP = OFF	Disabled

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18C601

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = EC	EC Oscillator
OSC = HS	HS Oscillator
OSC = RC	RC Oscillator

Power-up Timer:

PWRT = ON	Enable
PWRT = OFF	Disable

External Bus Data Width:

BW = 8	8-bit External Bus mode
BW = 16	16-bit External Bus mode

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Timer Postscale Selection:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Full/Underflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18C658

Code Protect:

CP = ON	Enabled
CP = OFF	Disabled

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18C801

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = EC	EC Oscillator
OSC = HS	HS Oscillator
OSC = RC	RC Oscillator

Power-up Timer:

PWRT = ON	Enable
PWRT = OFF	Disable

External Bus Data Width:

BW = 8	8-bit External Bus mode
BW = 16	16-bit External Bus mode

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Timer Postscale Selection:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Full/Underflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18C858

Code Protect:

CP = ON	Enabled
CP = OFF	Disabled

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

PIC18F1220

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Full/Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F1230

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Configuration Settings

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

FLTA MUX Bit:

FLTAMX = RA7	Multiplexed with RA7
FLTAMX = RA5	Multiplexed with RA5

T1OSC MUX bit:

T1OSCMX = LOW	T1OSC pins reside on RB2 and RB3
T1OSCMX = HIGH	T1OSC pins reside on RA6 and RA7

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Overflow Reset Enable Bit:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Dedicated In-Circuit Port Enable Bit:

ENICPORT = OFF	Disabled
ENICPORT = ON	Enabled

Boot Block Size Select Bits:

BBSIZ = BB256	256 W Boot Block Size
BBSIZ = BB512	512 W Boot Block Size

Extended Instruction Set Enable bit:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F1231

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Configuration Settings

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

FLTA MUX Bit:

FLTAMX = RA7	Multiplexed with RA7
FLTAMX = RA5	Multiplexed with RA5

T1OSC MUX bit:

T1OSCMX = LOW	T1OSC pins reside on RB2 and RB3
T1OSCMX = HIGH	T1OSC pins reside on RA6 and RA7

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Overflow Reset Enable Bit:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Dedicated In-Circuit Port Enable Bit:

ENICPORT = OFF	Disabled
ENICPORT = ON	Enabled

Boot Block Size Select Bits:

BBSIZ = BB256	256 W Boot Block Size
BBSIZ = BB512	512 W Boot Block Size

Extended Instruction Set Enable bit:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F1320

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Configuration Settings

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Full/Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRD = ON	Enabled
WRD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F1330

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

FLTA MUX Bit:

FLTAMX = RA7	Multiplexed with RA7
FLTAMX = RA5	Multiplexed with RA5

T1OSC MUX bit:

T1OSCMX = LOW	T1OSC pins reside on RB2 and RB3
T1OSCMX = HIGH	T1OSC pins reside on RA6 and RA7

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Overflow Reset Enable Bit:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Dedicated In-Circuit Port Enable Bit:

ENICPORT = OFF	Disabled
ENICPORT = ON	Enabled

Boot Block Size Select Bits:

BBSIZ = BB256	256 W Boot Block Size
BBSIZ = BB512	512 W Boot Block Size
BBSIZ = BB1K	1 KW Boot Block Size

Extended Instruction Set Enable bit:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F1331

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

FLTA MUX Bit:

FLTAMX = RA7	Multiplexed with RA7
FLTAMX = RA5	Multiplexed with RA5

T1OSC MUX bit:

T1OSCMX = LOW	T1OSC pins reside on RB2 and RB3
T1OSCMX = HIGH	T1OSC pins reside on RA6 and RA7

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Overflow Reset Enable Bit:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Dedicated In-Circuit Port Enable Bit:

ENICPORT = OFF	Disabled
ENICPORT = ON	Enabled

Boot Block Size Select Bits:

BBSIZ = BB256	256 W Boot Block Size
BBSIZ = BB512	512 W Boot Block Size
BBSIZ = BB1K	1 KW Boot Block Size

Extended Instruction Set Enable bit:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2220

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBAD = DIG	Digital
PBAD = ANA	Analog

CCP2 Pin Function:

CCP2MX = B3	RB3
CCP2MX = OFF	RB3
CCP2MX = C1	RC1
CCP2MX = ON	RC1

Stack Full/Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2221

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Disabled
FSCM = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled Always
BOR = SOFT	Enabled by SBOREN
BOR = NOSLP	Enabled except in Sleep
BOR = ON	Enabled Always

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = HIGH	High Power - High Noise Immunity
LPT1OSC = LOW	Low Power - Low Noise Immunity

PORTB A/D Enable:

PBAD = DIG	PORTB<4:0> digital on Reset
PBAD = ANA	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = RB3	Multiplexed with RB3
CCP2MX = RC1	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

ICD Port Enable:

ICPORT = OFF	Disabled
ICPORT = ON	Enabled

Boot Block Size:

BBSIZ = BB256	256 Word
BBSIZ = BB512	512 Word

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Protected
CP0 = OFF	Open

Code Protection Block 1:

CP1 = ON	Protected
CP1 = OFF	Open

Code Protect - Boot Block:

CPB = ON	Protected
CPB = OFF	Open

Code Protect - Data EEPROM:

CPD = ON	Protected
CPD = OFF	Open

Write Protection Block 0:

WRT0 = ON	Protected
WRT0 = OFF	Open

Write Protection Block 1:

WRT1 = ON	Protected
WRT1 = OFF	Open

Configuration Register Write Protection:

WRTC = ON	Protected
WRTC = OFF	Open

Boot Block Write Protection :

WRTB = ON	Protected
WRTB = OFF	Open

Data EEPROM Write Protection:

WRTD = ON	Protected
WRTD = OFF	Open

Table Read Protection Block 0:

EBTR0 = ON	Protected
EBTR0 = OFF	Open

Table Read Protection Block 1:

EBTR1 = ON	Protected
EBTR1 = OFF	Open

Boot Block Table Read Protection:

EBTRB = ON	Protected
EBTRB = OFF	Open

PIC18F2320

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBAD = DIG	Digital
PBAD = ANA	Analog

CCP2 Pin Function:

CCP2MX = B3	RB3
CCP2MX = OFF	RB3
CCP2MX = C1	RC1
CCP2MX = ON	RC1

Stack Full/Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2321

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Disabled
FSCM = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled Always
BOR = SOFT	Enabled by SBOREN
BOR = NOSLP	Enabled except in Sleep
BOR = ON	Enabled Always

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = HIGH	High Power - High Noise Immunity
LPT1OSC = LOW	Low Power - Low Noise Immunity

PORTB A/D Enable:

PBAD = DIG	PORTB<4:0> digital on Reset
PBAD = ANA	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = RB3	Multiplexed with RB3
CCP2MX = RC1	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

ICD Port Enable:

ICPORT = OFF	Disabled
ICPORT = ON	Enabled

Boot Block Size:

BBSIZ = BB256	256 Word
BBSIZ = BB512	512 Word
BBSIZ = BB1K	1024 Word

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Protected
CP0 = OFF	Open

Code Protection Block 1:

CP1 = ON	Protected
CP1 = OFF	Open

Code Protect - Boot Block:

CPB = ON	Protected
CPB = OFF	Open

Code Protect - Data EEPROM:

CPD = ON	Protected
CPD = OFF	Open

Write Protection Block 0:

WRT0 = ON	Protected
WRT0 = OFF	Open

Write Protection Block 1:

WRT1 = ON	Protected
WRT1 = OFF	Open

Configuration Register Write Protection:

WRTC = ON	Protected
WRTC = OFF	Open

Boot Block Write Protection :

WRTB = ON	Protected
WRTB = OFF	Open

Data EEPROM Write Protection:

WRTD = ON	Protected
WRTD = OFF	Open

Table Read Protection Block 0:

EBTR0 = ON	Protected
EBTR0 = OFF	Open

Table Read Protection Block 1:

EBTR1 = ON	Protected
EBTR1 = OFF	Open

Boot Block Table Read Protection:

EBTRB = ON	Protected
EBTRB = OFF	Open

PIC18F2331

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC2	External RC, RA6 is CLKOUT
OSC = EC	EC, RA6 is CLKOUT
OSC = ECIO	EC, RA6 is I/O
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	External RC, RA6 is I/O
OSC = IRCIO	Internal RC, RA6 & RA7 are I/O
OSC = IRC	Internal RC, RA6 is CLKOUT, RA7 is I/O
OSC = RC1	External RC, RA6 is CLKOUT
OSC = RC	External RC, RA6 is CLKOUT

Fail-Safe Clock Monitor Enable:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch-Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRTEN = ON	Enabled
PWRTEN = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Watchdog Timer Enable Window:

WINEN = ON	Enabled
WINEN = OFF	Disabled

Watchdog Postscaler:

WDPS = 1	1:1
WDPS = 2	1:2
WDPS = 4	1:4
WDPS = 8	1:8
WDPS = 16	1:16
WDPS = 32	1:32
WDPS = 64	1:64
WDPS = 128	1:128
WDPS = 256	1:256
WDPS = 512	1:512
WDPS = 1024	1:1024
WDPS = 2048	1:2048
WDPS = 4096	1:4096
WDPS = 8192	1:8192
WDPS = 16384	1:16384
WDPS = 32768	1:32768

Timer1 Oscillator MUX:

T1OSCMX = OFF	Active
T1OSCMX = ON	Inactive

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Configuration Settings

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2410

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F242

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2420

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2431

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC2	External RC, RA6 is CLKOUT
OSC = EC	EC, RA6 is CLKOUT
OSC = ECIO	EC, RA6 is I/O
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	External RC, RA6 is I/O
OSC = IRCIO	Internal RC, RA6 & RA7 are I/O
OSC = IRC	Internal RC, RA6 is CLKOUT, RA7 is I/O
OSC = RC1	External RC, RA6 is CLKOUT
OSC = RC	External RC, RA6 is CLKOUT

Fail-Safe Clock Monitor Enable:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch-Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRTEN = ON	Enabled
PWRTEN = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Watchdog Timer Enable Window:

WINEN = ON	Enabled
WINEN = OFF	Disabled

Watchdog Postscaler:

WDPS = 1	1:1
WDPS = 2	1:2
WDPS = 4	1:4
WDPS = 8	1:8
WDPS = 16	1:16
WDPS = 32	1:32
WDPS = 64	1:64
WDPS = 128	1:128
WDPS = 256	1:256
WDPS = 512	1:512
WDPS = 1024	1:1024
WDPS = 2048	1:2048
WDPS = 4096	1:4096
WDPS = 8192	1:8192
WDPS = 16384	1:16384
WDPS = 32768	1:32768

Timer1 Oscillator MUX:

T1OSCMX = OFF	Active
T1OSCMX = ON	Inactive

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2439

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2450

96 MHz PLL Prescaler:

PLLDIV = 1	No divide (4 MHz input)
PLLDIV = 2	Divide by 2 (8 MHz input)
PLLDIV = 3	Divide by 3 (12 MHz input)
PLLDIV = 4	Divide by 4 (16 MHz input)
PLLDIV = 5	Divide by 5 (20 MHz input)
PLLDIV = 6	Divide by 6 (24 MHz input)
PLLDIV = 10	Divide by 10 (40 MHz input)
PLLDIV = 12	Divide by 12 (48 MHz input)

CPU System Clock Postscaler:

CPUDIV = OSC1_PLL2	[OSC1/OSC2 Src: /1][96 MHz PLL Src: /2]
CPUDIV = OSC2_PLL3	[OSC1/OSC2 Src: /2][96 MHz PLL Src: /3]
CPUDIV = OSC3_PLL4	[OSC1/OSC2 Src: /3][96 MHz PLL Src: /4]
CPUDIV = OSC4_PLL6	[OSC1/OSC2 Src: /4][96 MHz PLL Src: /6]

Full-Speed USB Clock Source Selection:

USBDIV = 1	Clock source from OSC1/OSC2
USBDIV = 2	Clock source from 96 MHz PLL/2

Oscillator Selection bits:

FOSC = XT_XT	XT oscillator, XT used by USB
FOSC = XTPLL_XT	XT oscillator, PLL enabled, XT used by USB
FOSC = ECIO_EC	External clock, port function on RA6, EC used by USB
FOSC = EC_EC	External clock, CLKOUT on RA6, EC used by USB
FOSC = ECPLLIO_EC	External clock, PLL enabled, port function on RA6, EC used by USB
FOSC = ECPLL_EC	External clock, PLL enabled, CLKOUT on RA6, EC used by USB
FOSC = INTOSCIO_EC	Internal oscillator, port function on RA6, EC used by USB
FOSC = INTOSC_EC	Internal oscillator, CLKOUT on RA6, EC used by USB
FOSC = INTOSC_XT	Internal oscillator, XT used by USB
FOSC = INTOSC_HS	Internal oscillator, HS used by USB
FOSC = HS	HS oscillator, HS used by USB
FOSC = HSPLL_HS	HS oscillator, PLL enabled, HS used by USB

Fail-Safe Clock Monitor:

FCMEM = OFF	Disabled
FCMEM = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SOFT	Controlled by SBOREN
BOR = ON_ACTIVE	Enabled when the device is not in Sleep, SBOREN bit is disabled
BOR = ON	Enabled, SBOREN bit is disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

USB Voltage Regulator Enable:

VREGEN = OFF	Disabled
VREGEN = ON	Enabled

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator Enable:

LPT1OSC = OFF	Timer1 oscillator configured for high power
LPT1OSC = ON	Timer1 oscillator configured for low power

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input on Reset

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size Select Bit:

BBSIZ = BB2K	2KW Boot Block Size
BBSIZ = BB1K	1KW Boot Block Size

Dedicated In-Circuit Debug/Programming Enable:

ICPRT = OFF	Disabled
ICPRT = ON	Enabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2455

96 MHz PLL Prescaler:

PLLDIV = 1	No divide (4 MHz input)
PLLDIV = 2	Divide by 2 (8 MHz input)
PLLDIV = 3	Divide by 3 (12 MHz input)
PLLDIV = 4	Divide by 4 (16 MHz input)
PLLDIV = 5	Divide by 5 (20 MHz input)
PLLDIV = 6	Divide by 6 (24 MHz input)
PLLDIV = 10	Divide by 10 (40 MHz input)
PLLDIV = 12	Divide by 12 (48 MHz input)

CPU System Clock Postscaler:

CPUDIV = OSC1_PLL2	[OSC1/OSC2 Src: /1][96 MHz PLL Src: /2]
CPUDIV = OSC2_PLL3	[OSC1/OSC2 Src: /2][96 MHz PLL Src: /3]
CPUDIV = OSC3_PLL4	[OSC1/OSC2 Src: /3][96 MHz PLL Src: /4]
CPUDIV = OSC4_PLL6	[OSC1/OSC2 Src: /4][96 MHz PLL Src: /6]

Full-Speed USB Clock Source Selection:

USBDIV = 1	Clock source from OSC1/OSC2
USBDIV = 2	Clock source from 96 MHz PLL/2

Oscillator Selection bits:

FOSC = XT_XT	XT oscillator, XT used by USB
FOSC = XTPLL_XT	XT oscillator, PLL enabled, XT used by USB
FOSC = ECIO_EC	External clock, port function on RA6, EC used by USB
FOSC = EC_EC	External clock, CLKOUT on RA6, EC used by USB
FOSC = ECPLLIO_EC	External clock, PLL enabled, port function on RA6, EC used by USB
FOSC = ECPLL_EC	External clock, PLL enabled, CLKOUT on RA6, EC used by USB
FOSC = INTOSCIO_EC	Internal oscillator, port function on RA6, EC used by USB
FOSC = INTOSC_EC	Internal oscillator, CLKOUT on RA6, EC used by USB
FOSC = INTOSC_XT	Internal oscillator, XT used by USB
FOSC = INTOSC_HS	Internal oscillator, HS used by USB
FOSC = HS	HS oscillator, HS used by USB
FOSC = HSPLL_HS	HS oscillator, PLL enabled, HS used by USB

Fail-Safe Clock Monitor:

FCMEM = OFF	Disabled
FCMEM = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Configuration Settings

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SOFT	Controlled by SBOREN
BOR = ON_ACTIVE	Enabled when the device is not in Sleep, SBOREN bit is disabled
BOR = ON	Enabled, SBOREN bit is disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

USB Voltage Regulator Enable:

VREGEN = OFF	Disabled
VREGEN = ON	Enabled

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator Enable:

LPT1OSC = OFF	Timer1 oscillator configured for high power
LPT1OSC = ON	Timer1 oscillator configured for low power

Configuration Settings

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input on Reset

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RB3
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Dedicated In-Circuit Debug/Programming Enable:

ICPRT = OFF	Disabled
ICPRT = ON	Enabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F248

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2480

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F24J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F2510

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Configuration Settings

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2515

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Configuration Settings

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F252

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2520

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Configuration Settings

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2525

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	Maximum
BORV = 43	High
BORV = 28	Low
BORV = 21	Minimum

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Configuration Settings

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2539

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2550

96 MHz PLL Prescaler:

PLLDIV = 1	No divide (4 MHz input)
PLLDIV = 2	Divide by 2 (8 MHz input)
PLLDIV = 3	Divide by 3 (12 MHz input)
PLLDIV = 4	Divide by 4 (16 MHz input)
PLLDIV = 5	Divide by 5 (20 MHz input)
PLLDIV = 6	Divide by 6 (24 MHz input)
PLLDIV = 10	Divide by 10 (40 MHz input)
PLLDIV = 12	Divide by 12 (48 MHz input)

CPU System Clock Postscaler:

CPUDIV = OSC1_PLL2	[OSC1/OSC2 Src: /1][96 MHz PLL Src: /2]
CPUDIV = OSC2_PLL3	[OSC1/OSC2 Src: /2][96 MHz PLL Src: /3]
CPUDIV = OSC3_PLL4	[OSC1/OSC2 Src: /3][96 MHz PLL Src: /4]
CPUDIV = OSC4_PLL6	[OSC1/OSC2 Src: /4][96 MHz PLL Src: /6]

Configuration Settings

Full-Speed USB Clock Source Selection:

USBDIV = 1	Clock source from OSC1/OSC2
USBDIV = 2	Clock source from 96 MHz PLL/2

Oscillator Selection bits:

FOSC = XT_XT	XT oscillator, XT used by USB
FOSC = XTPLL_XT	XT oscillator, PLL enabled, XT used by USB
FOSC = ECIO_EC	External clock, port function on RA6, EC used by USB
FOSC = EC_EC	External clock, CLKOUT on RA6, EC used by USB
FOSC = ECPLLIO_EC	External clock, PLL enabled, port function on RA6, EC used by USB
FOSC = ECPLL_EC	External clock, PLL enabled, CLKOUT on RA6, EC used by USB
FOSC = INTOSCIO_EC	Internal oscillator, port function on RA6, EC used by USB
FOSC = INTOSC_EC	Internal oscillator, CLKOUT on RA6, EC used by USB
FOSC = INTOSC_XT	Internal oscillator, XT used by USB
FOSC = INTOSC_HS	Internal oscillator, HS used by USB
FOSC = HS	HS oscillator, HS used by USB
FOSC = HSPLL_HS	HS oscillator, PLL enabled, HS used by USB

Fail-Safe Clock Monitor:

FCMEM = OFF	Disabled
FCMEM = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SOFT	Controlled by SBOREN
BOR = ON_ACTIVE	Enabled when the device is not in Sleep, SBOREN bit is disabled
BOR = ON	Enabled, SBOREN bit is disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

USB Voltage Regulator Enable:

VREGEN = OFF	Disabled
VREGEN = ON	Enabled

Configuration Settings

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator Enable:

LPT1OSC = OFF	Timer1 oscillator configured for high power
LPT1OSC = ON	Timer1 oscillator configured for low power

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input on Reset

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RB3
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Dedicated In-Circuit Debug/Programming Enable:

ICPRT = OFF	Disabled
ICPRT = ON	Enabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F258

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2580

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Configuration Settings

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2585

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

Configuration Settings

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block
BBSIZ = 4096	4K words (8K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F25J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Configuration Settings

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F2610

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2620

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	Maximum
BORV = 43	High
BORV = 28	Low
BORV = 21	Minimum

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F2680

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

Configuration Settings

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block
BBSIZ = 4096	4K words (8K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4220

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBAD = DIG	Digital
PBAD = ANA	Analog

CCP2 Pin Function:

CCP2MX = B3	RB3
CCP2MX = OFF	RB3
CCP2MX = C1	RC1
CCP2MX = ON	RC1

Stack Full/Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4221

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Disabled
FSCM = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled Always
BOR = SOFT	Enabled by SBOREN
BOR = NOSLP	Enabled except in Sleep
BOR = ON	Enabled Always

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = HIGH	High Power - High Noise Immunity
LPT1OSC = LOW	Low Power - Low Noise Immunity

PORTB A/D Enable:

PBAD = DIG	PORTB<4:0> digital on Reset
PBAD = ANA	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = RB3	Multiplexed with RB3
CCP2MX = RC1	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

ICD Port Enable:

ICPORT = OFF	Disabled
ICPORT = ON	Enabled

Boot Block Size:

BBSIZ = BB256	256 Word
BBSIZ = BB512	512 Word

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Protected
CP0 = OFF	Open

Code Protection Block 1:

CP1 = ON	Protected
CP1 = OFF	Open

Code Protect - Boot Block:

CPB = ON	Protected
CPB = OFF	Open

Code Protect - Data EEPROM:

CPD = ON	Protected
CPD = OFF	Open

Write Protection Block 0:

WRT0 = ON	Protected
WRT0 = OFF	Open

Write Protection Block 1:

WRT1 = ON	Protected
WRT1 = OFF	Open

Configuration Register Write Protection:

WRTC = ON	Protected
WRTC = OFF	Open

Boot Block Write Protection :

WRTB = ON	Protected
WRTB = OFF	Open

Data EEPROM Write Protection:

WRTD = ON	Protected
WRTD = OFF	Open

Table Read Protection Block 0:

EBTR0 = ON	Protected
EBTR0 = OFF	Open

Table Read Protection Block 1:

EBTR1 = ON	Protected
EBTR1 = OFF	Open

Boot Block Table Read Protection:

EBTRB = ON	Protected
EBTRB = OFF	Open

PIC18F4320

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Fail-Safe Clock Monitor disabled
FSCM = ON	Fail-Safe Clock Monitor enabled

Internal External Switch Over mode:

IESO = OFF	Internal External Switch Over mode disabled
IESO = ON	Internal External Switch Over mode enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBAD = DIG	Digital
PBAD = ANA	Analog

CCP2 Pin Function:

CCP2MX = B3	RB3
CCP2MX = OFF	RB3
CCP2MX = C1	RC1
CCP2MX = ON	RC1

Stack Full/Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4321

Oscillator Selection:

OSC = LP	LP Oscillator
OSC = XT	XT Oscillator
OSC = HS	HS Oscillator
OSC = EC	External Clock on OSC1, OSC2 as FOSC/4
OSC = ECIO	External Clock on OSC1, OSC2 as RA6
OSC = HSPLL	HS + PLL
OSC = RCIO	External RC on OSC1, OSC2 as RA6
OSC = INTIO2	Internal RC, OSC1 as RA7, OSC2 as RA6
OSC = INTIO1	Internal RC, OSC1 as RA7, OSC2 as FOSC/4
OSC = RC	External RC on OSC1, OSC2 as FOSC/4

Fail-Safe Clock Monitor:

FSCM = OFF	Disabled
FSCM = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled Always
BOR = SOFT	Enabled by SBOREN
BOR = NOSLP	Enabled except in Sleep
BOR = ON	Enabled Always

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = HIGH	High Power - High Noise Immunity
LPT1OSC = LOW	Low Power - Low Noise Immunity

PORTB A/D Enable:

PBAD = DIG	PORTB<4:0> digital on Reset
PBAD = ANA	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = RB3	Multiplexed with RB3
CCP2MX = RC1	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

ICD Port Enable:

ICPORT = OFF	Disabled
ICPORT = ON	Enabled

Boot Block Size:

BBSIZ = BB256	256 Word
BBSIZ = BB512	512 Word
BBSIZ = BB1K	1024 Word

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Protected
CP0 = OFF	Open

Code Protection Block 1:

CP1 = ON	Protected
CP1 = OFF	Open

Code Protect - Boot Block:

CPB = ON	Protected
CPB = OFF	Open

Code Protect - Data EEPROM:

CPD = ON	Protected
CPD = OFF	Open

Write Protection Block 0:

WRT0 = ON	Protected
WRT0 = OFF	Open

Write Protection Block 1:

WRT1 = ON	Protected
WRT1 = OFF	Open

Configuration Register Write Protection:

WRTC = ON	Protected
WRTC = OFF	Open

Boot Block Write Protection :

WRTB = ON	Protected
WRTB = OFF	Open

Data EEPROM Write Protection:

WRTD = ON	Protected
WRTD = OFF	Open

Table Read Protection Block 0:

EBTR0 = ON	Protected
EBTR0 = OFF	Open

Table Read Protection Block 1:

EBTR1 = ON	Protected
EBTR1 = OFF	Open

Boot Block Table Read Protection:

EBTRB = ON	Protected
EBTRB = OFF	Open

PIC18F4331

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC2	External RC, RA6 is CLKOUT
OSC = EC	EC, RA6 is CLKOUT
OSC = ECIO	EC, RA6 is I/O
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	External RC, RA6 is I/O
OSC = IRCIO	Internal RC, RA6 & RA7 are I/O
OSC = IRC	Internal RC, RA6 is CLKOUT, RA7 is I/O
OSC = RC1	External RC, RA6 is CLKOUT
OSC = RC	External RC, RA6 is CLKOUT

Fail-Safe Clock Monitor Enable:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch-Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRTEN = ON	Enabled
PWRTEN = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Watchdog Timer Enable Window:

WINEN = ON	Enabled
WINEN = OFF	Disabled

Watchdog Postscaler:

WDPS = 1	1:1
WDPS = 2	1:2
WDPS = 4	1:4
WDPS = 8	1:8
WDPS = 16	1:16
WDPS = 32	1:32
WDPS = 64	1:64
WDPS = 128	1:128
WDPS = 256	1:256
WDPS = 512	1:512
WDPS = 1024	1:1024
WDPS = 2048	1:2048
WDPS = 4096	1:4096
WDPS = 8192	1:8192
WDPS = 16384	1:16384
WDPS = 32768	1:32768

Timer1 Oscillator MUX:

T1OSCMX = OFF	Active
T1OSCMX = ON	Inactive

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

External clock MUX bit:

EXCLKMX = RD0	Multiplexed with RD0
EXCLKMX = RC3	Multiplexed with RC3

PWM4 MUX bit:

PWM4MX = RD5	Multiplexed with RD5
PWM4MX = RB5	Multiplexed with RB5

SSP I/O MUX bit:

SSPMX = RD1	SDO output is multiplexed with RD1
SSPMX = RC7	SDO output is multiplexed with RC7

FLTA MUX bit:

FLTAMX = RD4	Multiplexed with RD4
FLTAMX = RC1	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4410

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F442

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Configuration Settings

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4420

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4431

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC2	External RC, RA6 is CLKOUT
OSC = EC	EC, RA6 is CLKOUT
OSC = ECIO	EC, RA6 is I/O
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	External RC, RA6 is I/O
OSC = IRCIO	Internal RC, RA6 & RA7 are I/O
OSC = IRC	Internal RC, RA6 is CLKOUT, RA7 is I/O
OSC = RC1	External RC, RA6 is CLKOUT
OSC = RC	External RC, RA6 is CLKOUT

Fail-Safe Clock Monitor Enable:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch-Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRTEN = ON	Enabled
PWRTEN = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Watchdog Timer Enable Window:

WINEN = ON	Enabled
WINEN = OFF	Disabled

Watchdog Postscaler:

WDPS = 1	1:1
WDPS = 2	1:2
WDPS = 4	1:4
WDPS = 8	1:8
WDPS = 16	1:16
WDPS = 32	1:32
WDPS = 64	1:64
WDPS = 128	1:128
WDPS = 256	1:256
WDPS = 512	1:512
WDPS = 1024	1:1024
WDPS = 2048	1:2048
WDPS = 4096	1:4096
WDPS = 8192	1:8192
WDPS = 16384	1:16384
WDPS = 32768	1:32768

Timer1 Oscillator MUX:

T1OSCMX = OFF	Active
T1OSCMX = ON	Inactive

High-Side Transistors Polarity:

HPOL = LOW	Active low
HPOL = HIGH	Active high

Low-Side Transistors Polarity:

LPOL = LOW	Active low
LPOL = HIGH	Active high

PWM output pins Reset state control:

PWMPIN = ON	Enabled
PWMPIN = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

External clock MUX bit:

EXCLKMX = RD0	Multiplexed with RD0
EXCLKMX = RC3	Multiplexed with RC3

PWM4 MUX bit:

PWM4MX = RD5	Multiplexed with RD5
PWM4MX = RB5	Multiplexed with RB5

SSP I/O MUX bit:

SSPMX = RD1	SDO output is multiplexed with RD1
SSPMX = RC7	SDO output is multiplexed with RC7

FLTA MUX bit:

FLTAMX = RD4	Multiplexed with RD4
FLTAMX = RC1	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4439

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Configuration Settings

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4450

96 MHz PLL Prescaler:

PLLDIV = 1	No divide (4 MHz input)
PLLDIV = 2	Divide by 2 (8 MHz input)
PLLDIV = 3	Divide by 3 (12 MHz input)
PLLDIV = 4	Divide by 4 (16 MHz input)
PLLDIV = 5	Divide by 5 (20 MHz input)
PLLDIV = 6	Divide by 6 (24 MHz input)
PLLDIV = 10	Divide by 10 (40 MHz input)
PLLDIV = 12	Divide by 12 (48 MHz input)

CPU System Clock Postscaler:

CPUDIV = OSC1_PLL2	[OSC1/OSC2 Src: /1][96 MHz PLL Src: /2]
CPUDIV = OSC2_PLL3	[OSC1/OSC2 Src: /2][96 MHz PLL Src: /3]
CPUDIV = OSC3_PLL4	[OSC1/OSC2 Src: /3][96 MHz PLL Src: /4]
CPUDIV = OSC4_PLL6	[OSC1/OSC2 Src: /4][96 MHz PLL Src: /6]

Configuration Settings

Full-Speed USB Clock Source Selection:

USBDIV = 1	Clock source from OSC1/OSC2
USBDIV = 2	Clock source from 96 MHz PLL/2

Oscillator Selection bits:

FOSC = XT_XT	XT oscillator, XT used by USB
FOSC = XTPLL_XT	XT oscillator, PLL enabled, XT used by USB
FOSC = ECIO_EC	External clock, port function on RA6, EC used by USB
FOSC = EC_EC	External clock, CLKOUT on RA6, EC used by USB
FOSC = ECPLLIO_EC	External clock, PLL enabled, port function on RA6, EC used by USB
FOSC = ECPLL_EC	External clock, PLL enabled, CLKOUT on RA6, EC used by USB
FOSC = INTOSCIO_EC	Internal oscillator, port function on RA6, EC used by USB
FOSC = INTOSC_EC	Internal oscillator, CLKOUT on RA6, EC used by USB
FOSC = INTOSC_XT	Internal oscillator, XT used by USB
FOSC = INTOSC_HS	Internal oscillator, HS used by USB
FOSC = HS	HS oscillator, HS used by USB
FOSC = HSPLL_HS	HS oscillator, PLL enabled, HS used by USB

Fail-Safe Clock Monitor:

FCMEM = OFF	Disabled
FCMEM = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SOFT	Controlled by SBOREN
BOR = ON_ACTIVE	Enabled when the device is not in Sleep, SBOREN bit is disabled
BOR = ON	Enabled, SBOREN bit is disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

USB Voltage Regulator Enable:

VREGEN = OFF	Disabled
VREGEN = ON	Enabled

Configuration Settings

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator Enable:

LPT1OSC = OFF	Timer1 oscillator configured for high power
LPT1OSC = ON	Timer1 oscillator configured for low power

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input on Reset

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size Select Bit:

BBSIZ = BB2K	2KW Boot Block Size
BBSIZ = BB1K	1KW Boot Block Size

Dedicated In-Circuit Debug/Programming Enable:

ICPRT = OFF	Disabled
ICPRT = ON	Enabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4455

96 MHz PLL Prescaler:

PLLDIV = 1	No divide (4 MHz input)
PLLDIV = 2	Divide by 2 (8 MHz input)
PLLDIV = 3	Divide by 3 (12 MHz input)
PLLDIV = 4	Divide by 4 (16 MHz input)
PLLDIV = 5	Divide by 5 (20 MHz input)
PLLDIV = 6	Divide by 6 (24 MHz input)
PLLDIV = 10	Divide by 10 (40 MHz input)
PLLDIV = 12	Divide by 12 (48 MHz input)

CPU System Clock Postscaler:

CPUDIV = OSC1_PLL2	[OSC1/OSC2 Src: /1][96 MHz PLL Src: /2]
CPUDIV = OSC2_PLL3	[OSC1/OSC2 Src: /2][96 MHz PLL Src: /3]
CPUDIV = OSC3_PLL4	[OSC1/OSC2 Src: /3][96 MHz PLL Src: /4]
CPUDIV = OSC4_PLL6	[OSC1/OSC2 Src: /4][96 MHz PLL Src: /6]

Full-Speed USB Clock Source Selection:

USBDIV = 1	Clock source from OSC1/OSC2
USBDIV = 2	Clock source from 96 MHz PLL/2

Oscillator Selection bits:

FOSC = XT_XT	XT oscillator, XT used by USB
FOSC = XTPLL_XT	XT oscillator, PLL enabled, XT used by USB
FOSC = ECIO_EC	External clock, port function on RA6, EC used by USB
FOSC = EC_EC	External clock, CLKOUT on RA6, EC used by USB
FOSC = ECPLLIO_EC	External clock, PLL enabled, port function on RA6, EC used by USB
FOSC = ECPLL_EC	External clock, PLL enabled, CLKOUT on RA6, EC used by USB
FOSC = INTOSCIO_EC	Internal oscillator, port function on RA6, EC used by USB
FOSC = INTOSC_EC	Internal oscillator, CLKOUT on RA6, EC used by USB
FOSC = INTOSC_XT	Internal oscillator, XT used by USB
FOSC = INTOSC_HS	Internal oscillator, HS used by USB
FOSC = HS	HS oscillator, HS used by USB
FOSC = HSPLL_HS	HS oscillator, PLL enabled, HS used by USB

Fail-Safe Clock Monitor:

FCMEM = OFF	Disabled
FCMEM = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SOFT	Controlled by SBOREN
BOR = ON_ACTIVE	Enabled when the device is not in Sleep, SBOREN bit is disabled
BOR = ON	Enabled, SBOREN bit is disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

USB Voltage Regulator Enable:

VREGEN = OFF	Disabled
VREGEN = ON	Enabled

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator Enable:

LPT1OSC = OFF	Timer1 oscillator configured for high power
LPT1OSC = ON	Timer1 oscillator configured for low power

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input on Reset

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RB3
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Dedicated In-Circuit Debug/Programming Enable:

ICPRT = OFF	Disabled
ICPRT = ON	Enabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F448

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRD = ON	Enabled
WRD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4480

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F44J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Configuration Settings

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F4510

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Configuration Settings

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4515

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Configuration Settings

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F452

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disable (RB3)
CCP2MUX = ON	Enable (RC1)

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4520

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Configuration Settings

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4525

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	Maximum
BORV = 43	High
BORV = 28	Low
BORV = 21	Minimum

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4539

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4550

96 MHz PLL Prescaler:

PLLDIV = 1	No divide (4 MHz input)
PLLDIV = 2	Divide by 2 (8 MHz input)
PLLDIV = 3	Divide by 3 (12 MHz input)
PLLDIV = 4	Divide by 4 (16 MHz input)
PLLDIV = 5	Divide by 5 (20 MHz input)
PLLDIV = 6	Divide by 6 (24 MHz input)
PLLDIV = 10	Divide by 10 (40 MHz input)
PLLDIV = 12	Divide by 12 (48 MHz input)

CPU System Clock Postscaler:

CPUDIV = OSC1_PLL2	[OSC1/OSC2 Src: /1][96 MHz PLL Src: /2]
CPUDIV = OSC2_PLL3	[OSC1/OSC2 Src: /2][96 MHz PLL Src: /3]
CPUDIV = OSC3_PLL4	[OSC1/OSC2 Src: /3][96 MHz PLL Src: /4]
CPUDIV = OSC4_PLL6	[OSC1/OSC2 Src: /4][96 MHz PLL Src: /6]

Configuration Settings

Full-Speed USB Clock Source Selection:

USBDIV = 1	Clock source from OSC1/OSC2
USBDIV = 2	Clock source from 96 MHz PLL/2

Oscillator Selection bits:

FOSC = XT_XT	XT oscillator, XT used by USB
FOSC = XTPLL_XT	XT oscillator, PLL enabled, XT used by USB
FOSC = ECIO_EC	External clock, port function on RA6, EC used by USB
FOSC = EC_EC	External clock, CLKOUT on RA6, EC used by USB
FOSC = ECPLLIO_EC	External clock, PLL enabled, port function on RA6, EC used by USB
FOSC = ECPLL_EC	External clock, PLL enabled, CLKOUT on RA6, EC used by USB
FOSC = INTOSCIO_EC	Internal oscillator, port function on RA6, EC used by USB
FOSC = INTOSC_EC	Internal oscillator, CLKOUT on RA6, EC used by USB
FOSC = INTOSC_XT	Internal oscillator, XT used by USB
FOSC = INTOSC_HS	Internal oscillator, HS used by USB
FOSC = HS	HS oscillator, HS used by USB
FOSC = HSPLL_HS	HS oscillator, PLL enabled, HS used by USB

Fail-Safe Clock Monitor:

FCMEM = OFF	Disabled
FCMEM = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SOFT	Controlled by SBOREN
BOR = ON_ACTIVE	Enabled when the device is not in Sleep, SBOREN bit is disabled
BOR = ON	Enabled, SBOREN bit is disabled

Brown-out Voltage:

BORV = 46	4.6V
BORV = 43	4.3V
BORV = 28	2.8V
BORV = 21	2.1V

USB Voltage Regulator Enable:

VREGEN = OFF	Disabled
VREGEN = ON	Enabled

Configuration Settings

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator Enable:

LPT1OSC = OFF	Timer1 oscillator configured for high power
LPT1OSC = ON	Timer1 oscillator configured for low power

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input on Reset

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RB3
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Dedicated In-Circuit Debug/Programming Enable:

ICPRT = OFF	Disabled
ICPRT = ON	Enabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F458

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4580

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4585

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

Configuration Settings

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Enhanced Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block
BBSIZ = 4096	4K words (8K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F45J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Configuration Settings

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F4610

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Enhanced CPU Enable:

ENHCPU = OFF	Disabled
ENHCPU = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4620

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	Maximum
BORV = 43	High
BORV = 28	Low
BORV = 21	Minimum

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

PORTB A/D Enable:

PBADEN = OFF	PORTB<4:0> digital on Reset
PBADEN = ON	PORTB<4:0> analog on Reset

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F4680

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	External RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	External RC with OSC2 as RA6
OSC = IRCIO67	Internal RC with OSC2 as RA6 and OSC1 as RA7
OSC = IRCIO7	Internal RC with OSC1 as RA7 and OSC2 as divide by 4 clock out
OSC = ERC1	External RC with OSC2 as divide by 4 clock out
OSC = ERC	External RC with OSC2 as divide by 4 clock out

Fail-Safe Clock Monitor:

FCMENB = OFF	Disabled
FCMENB = ON	Enabled

Internal External Osc. Switch:

IESOB = OFF	Disabled
IESOB = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = SBORENCTRL	Controlled by SBOREN
BOR = BOACTIVE	Enabled whenever Part is Active - SBOREN Disabled
BOR = BOHW	Enabled in HW, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Oscillator:

LPT1OSC = OFF	Timer1 Low Power Oscillator Disabled
LPT1OSC = ON	Timer1 Low Power Oscillator Active

PORTB Pins Configured for A/D:

PBADEN = OFF	PORTB<4> and PORTB<1:0> Configured as Digital I/O Pins on Reset
PBADEN = ON	PORTB<4> and PORTB<1:0> Configured as Analog Pins on Reset

BackGround Debug:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Enhanced Instruction Set CPU:

XINST = OFF	Disabled
XINST = ON	Enabled

Boot Block Size:

BBSIZ = 1024	1K words (2K bytes) Boot Block
BBSIZ = 2048	2K words (4K bytes) Boot Block
BBSIZ = 4096	4K words (8K bytes) Boot Block

Low Voltage Programming:

LVP = OFF	Disabled
LVP = ON	Enabled

Stack Overflow/Underflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6310

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F6390

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F6410

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F6490

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F64J15

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F6520

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Uses RE7
CCP2MUX = RE7	Uses RE7
CCP2MUX = ON	Uses RC1
CCP2MUX = RC1	Uses RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6525

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = ECIOPLL	EC-OSC2 as RA6 and PLL
OSC = ECIOPLL	EC-OSC2 as RA6 and SW PLL
OSC = HSSWPLL	HS with SW PLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3 or RE7
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6527

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP2 MUX:

CCP2MX = PORTB	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6585

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	RC with OSC2 as RA6
OSC = ECIOPLL	EC with OSC2 as RA6 and HW enabled 4xPLL
OSC = ECIOPLL	EC with OSC2 as RA6 and SW enabled 4xPLL
OSC = HSSWPLL	HS with SW enabled 4xPLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RE7
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F65J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F65J15

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Configuration Settings

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F6620

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Configuration Settings

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disabled
CCP2MUX = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6621

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = ECIOPLL	EC-OSC2 as RA6 and PLL
OSC = ECIOPLL	EC-OSC2 as RA6 and SW PLL
OSC = HSSWPLL	HS with SW PLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3 or RE7
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6622

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP2 MUX:

CCP2MX = PORTB	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Configuration Settings

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6627

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Configuration Settings

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Code Protection Block 4:

CP4 = ON	Enabled
CP4 = OFF	Disabled

Code Protection Block 5:

CP5 = ON	Enabled
CP5 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Write Protection Block 4:

WRT4 = ON	Enabled
WRT4 = OFF	Disabled

Write Protection Block 5:

WRT5 = ON	Enabled
WRT5 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Table Read Protection Block 4:

EBTR4 = ON	Enabled
EBTR4 = OFF	Disabled

Table Read Protection Block 5:

EBTR5 = ON	Enabled
EBTR5 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6680

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	RC with OSC2 as RA6
OSC = ECIOPLL	EC with OSC2 as RA6 and HW enabled 4xPLL
OSC = ECIOPLL	EC with OSC2 as RA6 and SW enabled 4xPLL
OSC = HSSWPLL	HS with SW enabled 4xPLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RE7
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRD = ON	Enabled
WRD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F66J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F66J15

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Configuration Settings

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F66J60

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Configuration Settings

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

PIC18F66J65

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

PIC18F6720

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

CCP2 MUX:

CCP2MUX = OFF	Disabled
CCP2MUX = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Code Protection Block 4:

CP4 = ON	Enabled
CP4 = OFF	Disabled

Code Protection Block 5:

CP5 = ON	Enabled
CP5 = OFF	Disabled

Code Protection Block 6:

CP6 = ON	Enabled
CP6 = OFF	Disabled

Code Protection Block 7:

CP7 = ON	Enabled
CP7 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Write Protection Block 4:

WRT4 = ON	Enabled
WRT4 = OFF	Disabled

Write Protection Block 5:

WRT5 = ON	Enabled
WRT5 = OFF	Disabled

Write Protection Block 6:

WRT6 = ON	Enabled
WRT6 = OFF	Disabled

Write Protection Block 7:

WRT7 = ON	Enabled
WRT7 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Table Read Protection Block 4:

EBTR4 = ON	Enabled
EBTR4 = OFF	Disabled

Table Read Protection Block 5:

EBTR5 = ON	Enabled
EBTR5 = OFF	Disabled

Table Read Protection Block 6:

EBTR6 = ON	Enabled
EBTR6 = OFF	Disabled

Table Read Protection Block 7:

EBTR7 = ON	Enabled
EBTR7 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F6722

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Code Protection Block 4:

CP4 = ON	Enabled
CP4 = OFF	Disabled

Code Protection Block 5:

CP5 = ON	Enabled
CP5 = OFF	Disabled

Code Protection Block 6:

CP6 = ON	Enabled
CP6 = OFF	Disabled

Code Protection Block 7:

CP7 = ON	Enabled
CP7 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Write Protection Block 4:

WRT4 = ON	Enabled
WRT4 = OFF	Disabled

Write Protection Block 5:

WRT5 = ON	Enabled
WRT5 = OFF	Disabled

Write Protection Block 6:

WRT6 = ON	Enabled
WRT6 = OFF	Disabled

Write Protection Block 7:

WRT7 = ON	Enabled
WRT7 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Table Read Protection Block 4:

EBTR4 = ON	Enabled
EBTR4 = OFF	Disabled

Table Read Protection Block 5:

EBTR5 = ON	Enabled
EBTR5 = OFF	Disabled

Table Read Protection Block 6:

EBTR6 = ON	Enabled
EBTR6 = OFF	Disabled

Table Read Protection Block 7:

EBTR7 = ON	Enabled
EBTR7 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F67J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F67J60

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Configuration Settings

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

PIC18F8310

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

PM = EM	Extended Microcontroller mode
PM = MPB	Microprocessor with Boot Block mode
PM = MP	Microprocessor mode
PM = MC	Microcontroller mode

External Data Bus Width:

BW = 8	8-bit External Data Bus Width
BW = 16	16-bit External Data Bus Width

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Configuration Settings

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F8390

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F8410

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

PM = EM	Extended Microcontroller mode
PM = MPB	Microprocessor with Boot Block mode
PM = MP	Microprocessor mode
PM = MC	Microcontroller mode

External Data Bus Width:

BW = 8	8-bit External Data Bus Width
BW = 16	16-bit External Data Bus Width

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F8490

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

Low Power Timer1 Selection:

LPT1OSC = OFF	High Power, High noise immunity T1OSC selected
LPT1OSC = ON	Low Power, Low noise immunity T1OSC selected

CCP2 MUX:

CCP2MX = PORTBE	CCP2 input/output is multiplexed with RE7/RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Extended Instruction set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection:

CP = ON	Enabled
CP = OFF	Disabled

Table Read Protection Internal Memory:

EBTR = ON	Enabled
EBTR = OFF	Disabled

PIC18F84J15

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

ECCP MUX:

ECCPMX = ALTERNATE	Multiplexed with RH7:4
ECCPMX = DEFAULT	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F8520

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC-OSC2 as Clock Out
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

CCP2 MUX:

CCP2MUX = OFF	Uses RE7
CCP2MUX = RE7	Uses RE7
CCP2MUX = ON	Uses RC1
CCP2MUX = RC1	Uses RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8525

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = ECIOPLL	EC-OSC2 as RA6 and PLL
OSC = ECIO SWPLL	EC-OSC2 as RA6 and SW PLL
OSC = HSSWPLL	HS with SW PLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3 or RE7
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8527

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Address Width:

ADDRBW = ADDR8BIT	8-bit Address Bus
ADDRBW = ADDR12BIT	12-bit Address Bus
ADDRBW = ADDR16BIT	16-bit Address Bus
ADDRBW = ADDR20BIT	20-bit Address Bus

External Bus Data Width:

DATABW = DATA8BIT	8-bit Data Bus
DATABW = DATA16BIT	16-bit Data Bus

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

ECCP2 MUX:

CCP2MX = PORTB	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8585

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	RC with OSC2 as RA6
OSC = ECIOPLL	EC with OSC2 as RA6 and HW enabled 4xPLL
OSC = ECIOPLL	EC with OSC2 as RA6 and SW enabled 4xPLL
OSC = HSSWPLL	HS with SW enabled 4xPLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Configuration Settings

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RE7
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F85J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Configuration Settings

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

Configuration Settings

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

ECCP MUX:

ECCPMX = ALTERNATE	Multiplexed with RH7:4
ECCPMX = DEFAULT	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F85J15

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

ECCP MUX:

ECCPMX = ALTERNATE	Multiplexed with RH7:4
ECCPMX = DEFAULT	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F8620

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

CCP2 MUX:

CCP2MUX = OFF	Disabled
CCP2MUX = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8621

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6
OSC = ECIOPLL	EC-OSC2 as RA6 and PLL
OSC = ECIO SWPLL	EC-OSC2 as RA6 and SW PLL
OSC = HSSWPLL	HS with SW PLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3 or RE7
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Configuration Settings

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8622

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Address Width:

ADDRBW = ADDR8BIT	8-bit Address Bus
ADDRBW = ADDR12BIT	12-bit Address Bus
ADDRBW = ADDR16BIT	16-bit Address Bus
ADDRBW = ADDR20BIT	20-bit Address Bus

External Bus Data Width:

DATABW = DATA8BIT	8-bit Data Bus
DATABW = DATA16BIT	16-bit Data Bus

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

ECCP2 MUX:

CCP2MX = PORTB	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8627

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Address Width:

ADDRBW = ADDR8BIT	8-bit Address Bus
ADDRBW = ADDR12BIT	12-bit Address Bus
ADDRBW = ADDR16BIT	16-bit Address Bus
ADDRBW = ADDR20BIT	20-bit Address Bus

External Bus Data Width:

DATABW = DATA8BIT	8-bit Data Bus
DATABW = DATA16BIT	16-bit Data Bus

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

ECCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Code Protection Block 4:

CP4 = ON	Enabled
CP4 = OFF	Disabled

Code Protection Block 5:

CP5 = ON	Enabled
CP5 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Write Protection Block 4:

WRT4 = ON	Enabled
WRT4 = OFF	Disabled

Write Protection Block 5:

WRT5 = ON	Enabled
WRT5 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRD = ON	Enabled
WRD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Table Read Protection Block 4:

EBTR4 = ON	Enabled
EBTR4 = OFF	Disabled

Table Read Protection Block 5:

EBTR5 = ON	Enabled
EBTR5 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8680

Oscillator Selection bits:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC with OSC2 as divide by 4 clock out
OSC = EC	EC with OSC2 as divide by 4 clock out
OSC = ECIO	EC with OSC2 as RA6
OSC = HSPLL	HS with HW enabled 4xPLL
OSC = RCIO	RC with OSC2 as RA6
OSC = ECIOPLL	EC with OSC2 as RA6 and HW enabled 4xPLL
OSC = ECIOPLL	EC with OSC2 as RA6 and SW enabled 4xPLL
OSC = HSSWPLL	HS with SW enabled 4xPLL

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 20	2.0V

Watchdog Timer:

WDT = OFF	HW Disabled - SW Controlled
WDT = ON	HW Enabled - SW Disabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

CCP2 MUX bit:

CCP2MX = OFF	CCP2 input/output is multiplexed with RE7
CCP2MX = ON	CCP2 input/output is multiplexed with RC1

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Configuration Settings

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F86J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Configuration Settings

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

ECCP MUX:

ECCPMX = ALTERNATE	Multiplexed with RH7:4
ECCPMX = DEFAULT	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F86J15

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

ECCP MUX:

ECCPMX = ALTERNATE	Multiplexed with RH7:4
ECCPMX = DEFAULT	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F86J60

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

ECCP MUX:

ECCPMX = OFF	Disabled
ECCPMX = ON	Enabled

CCP2 MUX:

CCP2MX = OFF	Disabled
CCP2MX = ON	Enabled

PIC18F86J65

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Configuration Settings

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

ECCP MUX:

ECCPMX = OFF	Disabled
ECCPMX = ON	Enabled

CCP2 MUX:

CCP2MX = OFF	Disabled
CCP2MX = ON	Enabled

PIC18F8720

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO	RC-OSC2 as RA6

Osc. Switch Enable:

OSCS = ON	Enabled
OSCS = OFF	Disabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOR = OFF	Disabled
BOR = ON	Enabled

Brown-out Voltage:

BORV = 45	4.5V
BORV = 42	4.2V
BORV = 27	2.7V
BORV = 25	2.5V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

CCP2 MUX:

CCP2MUX = OFF	Disabled
CCP2MUX = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Code Protection Block 4:

CP4 = ON	Enabled
CP4 = OFF	Disabled

Code Protection Block 5:

CP5 = ON	Enabled
CP5 = OFF	Disabled

Code Protection Block 6:

CP6 = ON	Enabled
CP6 = OFF	Disabled

Code Protection Block 7:

CP7 = ON	Enabled
CP7 = OFF	Disabled

Configuration Settings

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Write Protection Block 4:

WRT4 = ON	Enabled
WRT4 = OFF	Disabled

Write Protection Block 5:

WRT5 = ON	Enabled
WRT5 = OFF	Disabled

Write Protection Block 6:

WRT6 = ON	Enabled
WRT6 = OFF	Disabled

Write Protection Block 7:

WRT7 = ON	Enabled
WRT7 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTD = ON	Enabled
WRTD = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Table Read Protection Block 4:

EBTR4 = ON	Enabled
EBTR4 = OFF	Disabled

Table Read Protection Block 5:

EBTR5 = ON	Enabled
EBTR5 = OFF	Disabled

Table Read Protection Block 6:

EBTR6 = ON	Enabled
EBTR6 = OFF	Disabled

Table Read Protection Block 7:

EBTR7 = ON	Enabled
EBTR7 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F8722

Oscillator Selection:

OSC = LP	LP
OSC = XT	XT
OSC = HS	HS
OSC = RC	RC
OSC = EC	EC-OSC2 as Clock Out
OSC = ECIO6	EC-OSC2 as RA6
OSC = HSPLL	HS-PLL Enabled
OSC = RCIO6	RC-OSC2 as RA6
OSC = INTIO67	INTRC-OSC2 as RA6, OSC1 as RA7
OSC = INTIO7	INTRC-OSC2 as Clock Out, OSC1 as RA7

Configuration Settings

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal External Osc. Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Power-up Timer:

PWRT = ON	Enabled
PWRT = OFF	Disabled

Brown-out Reset:

BOREN = OFF	Disabled
BOREN = ON	SBOREN Enabled
BOREN = NOSLP	Enabled except Sleep, SBOREN Disabled
BOREN = SBORDIS	Enabled, SBOREN Disabled

Brown-out Voltage:

BORV = 46	4.5V
BORV = 43	4.2V
BORV = 28	2.7V
BORV = 21	2.0V

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Processor Mode Selection:

MODE = EM	Extended Microcontroller mode
MODE = MPB	Microprocessor with Boot Block mode
MODE = MP	Microprocessor mode
MODE = MC	Microcontroller mode

External Bus Address Width:

ADDRBW = ADDR8BIT	8-bit Address Bus
ADDRBW = ADDR12BIT	12-bit Address Bus
ADDRBW = ADDR16BIT	16-bit Address Bus
ADDRBW = ADDR20BIT	20-bit Address Bus

External Bus Data Width:

DATABW = DATA8BIT	8-bit Data Bus
DATABW = DATA16BIT	16-bit Data Bus

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

MCLR Enable:

MCLRE = OFF	Disabled
MCLRE = ON	Enabled

T1 Oscillator Enable:

LPT1OSC = OFF	Disabled
LPT1OSC = ON	Enabled

ECCP MUX:

ECCPMX = PORTH	Multiplexed with RH7:4
ECCPMX = PORTE	Multiplexed with RE6:3

ECCP2 MUX:

CCP2MX = PORTBE	Multiplexed with RB3
CCP2MX = PORTC	Multiplexed with RC1

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Boot Block Size:

BBSIZ = BB2K	2Kb Boot Block
BBSIZ = BB4K	4Kb Boot Block
BBSIZ = BB8K	8Kb Boot Block

XINST Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Code Protection Block 0:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Code Protection Block 1:

CP1 = ON	Enabled
CP1 = OFF	Disabled

Code Protection Block 2:

CP2 = ON	Enabled
CP2 = OFF	Disabled

Code Protection Block 3:

CP3 = ON	Enabled
CP3 = OFF	Disabled

Code Protection Block 4:

CP4 = ON	Enabled
CP4 = OFF	Disabled

Code Protection Block 5:

CP5 = ON	Enabled
CP5 = OFF	Disabled

Code Protection Block 6:

CP6 = ON	Enabled
CP6 = OFF	Disabled

Code Protection Block 7:

CP7 = ON	Enabled
CP7 = OFF	Disabled

Boot Block Code Protection:

CPB = ON	Enabled
CPB = OFF	Disabled

Data EEPROM Code Protection:

CPD = ON	Enabled
CPD = OFF	Disabled

Write Protection Block 0:

WRT0 = ON	Enabled
WRT0 = OFF	Disabled

Write Protection Block 1:

WRT1 = ON	Enabled
WRT1 = OFF	Disabled

Write Protection Block 2:

WRT2 = ON	Enabled
WRT2 = OFF	Disabled

Write Protection Block 3:

WRT3 = ON	Enabled
WRT3 = OFF	Disabled

Write Protection Block 4:

WRT4 = ON	Enabled
WRT4 = OFF	Disabled

Write Protection Block 5:

WRT5 = ON	Enabled
WRT5 = OFF	Disabled

Write Protection Block 6:

WRT6 = ON	Enabled
WRT6 = OFF	Disabled

Write Protection Block 7:

WRT7 = ON	Enabled
WRT7 = OFF	Disabled

Boot Block Write Protection:

WRTB = ON	Enabled
WRTB = OFF	Disabled

Configuration Register Write Protection:

WRTC = ON	Enabled
WRTC = OFF	Disabled

Data EEPROM Write Protection:

WRTE = ON	Enabled
WRTE = OFF	Disabled

Table Read Protection Block 0:

EBTR0 = ON	Enabled
EBTR0 = OFF	Disabled

Table Read Protection Block 1:

EBTR1 = ON	Enabled
EBTR1 = OFF	Disabled

Table Read Protection Block 2:

EBTR2 = ON	Enabled
EBTR2 = OFF	Disabled

Table Read Protection Block 3:

EBTR3 = ON	Enabled
EBTR3 = OFF	Disabled

Table Read Protection Block 4:

EBTR4 = ON	Enabled
EBTR4 = OFF	Disabled

Table Read Protection Block 5:

EBTR5 = ON	Enabled
EBTR5 = OFF	Disabled

Table Read Protection Block 6:

EBTR6 = ON	Enabled
EBTR6 = OFF	Disabled

Table Read Protection Block 7:

EBTR7 = ON	Enabled
EBTR7 = OFF	Disabled

Boot Block Table Read Protection:

EBTRB = ON	Enabled
EBTRB = OFF	Disabled

PIC18F87J10

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVREN = OFF	Disabled
STVREN = ON	Enabled

Watchdog Timer:

WDTEN = OFF	Disabled
WDTEN = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Configuration Settings

Default/Reset System Clock Select:

FOSC2 = OFF	When SCS1:SCS0 = 00, INTRC is the clock source
FOSC2 = ON	When SCS1:SCS0 = 00, FOSC1:FOSC0 sets the clock source

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

ECCP MUX:

ECCPMX = ALTERNATE	Multiplexed with RH7:4
ECCPMX = DEFAULT	Multiplexed with RE6:3

CCP2 MUX:

CCP2MX = ALTERNATE	Multiplexed with RB3
CCP2MX = DEFAULT	Multiplexed with RC1

PIC18F87J60

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

ECCP MUX:

ECCPMX = OFF	Disabled
ECCPMX = ON	Enabled

CCP2 MUX:

CCP2MX = OFF	Disabled
CCP2MX = ON	Enabled

PIC18F96J60

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Configuration Settings

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

ECCP MUX:

ECCPMX = OFF	Disabled
ECCPMX = ON	Enabled

CCP2 MUX:

CCP2MX = OFF	Disabled
CCP2MX = ON	Enabled

PIC18F96J65

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Configuration Settings

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

ECCP MUX:

ECCPMX = OFF	Disabled
ECCPMX = ON	Enabled

CCP2 MUX:

CCP2MX = OFF	Disabled
CCP2MX = ON	Enabled

PIC18F97J60

Background Debugger Enable:

DEBUG = ON	Enabled
DEBUG = OFF	Disabled

Extended Instruction Set Enable:

XINST = OFF	Disabled
XINST = ON	Enabled

Stack Overflow Reset:

STVR = OFF	Disabled
STVR = ON	Enabled

Watchdog Timer:

WDT = OFF	Disabled
WDT = ON	Enabled

Code Protection:

CP0 = ON	Enabled
CP0 = OFF	Disabled

Fail-Safe Clock Monitor:

FCMEN = OFF	Disabled
FCMEN = ON	Enabled

Internal/External Switch Over:

IESO = OFF	Disabled
IESO = ON	Enabled

Default/Reset System Clock Select Bit:

FOSC2 = OFF	INTRC as system clock when OSCCON<1:0> = 00
FOSC2 = ON	FOSC<1:0> selects system clock for OSCCON<1:0> = 00

Oscillator Selection bits:

FOSC = HS	HS oscillator
FOSC = HSPLL	HS oscillator, Software Controlled PLL
FOSC = EC	External Clock
FOSC = ECPLL	External Clock, Software Controlled PLL

Watchdog Postscaler:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

External Bus Data Wait:

WAIT = ON	Enabled
WAIT = OFF	Disabled

Data Bus Width Select:

BW = 8	8-bit external bus
BW = 16	16-bit external bus

Processor Mode Selection:

MODE = MM	Microcontroller mode - External bus disabled
MODE = XM12	Extended Microcontroller mode - 12-bit Address mode
MODE = XM16	Extended Microcontroller mode - 16-bit Address mode
MODE = XM20	Extended Microcontroller mode - 20-bit Address mode

External Address Bus Shift Enable:

EASHFT = OFF	External bus reflects PC value
EASHFT = ON	External bus starts at 000000h

Ethernet LED Enable:

ETHLED = OFF	Disabled
ETHLED = ON	Enabled

ECCP MUX:

ECCPMX = OFF	Disabled
ECCPMX = ON	Enabled

CCP2 MUX:

CCP2MX = OFF	Disabled
CCP2MX = ON	Enabled

PIC18LF2423

Oscillator Selection bits:

OSC = LP	LP oscillator
OSC = XT	XT oscillator
OSC = HS	HS oscillator
OSC = RC	External RC oscillator, CLKO function on RA6
OSC = EC	EC oscillator, CLKO function on RA6
OSC = ECIO6	EC oscillator, port function on RA6
OSC = HSPLL	HS oscillator, PLL enabled (Clock Frequency = 4 x FOSC1)
OSC = RCIO6	External RC oscillator, port function on RA6
OSC = INTIO67	Internal oscillator block, port function on RA6 and RA7
OSC = INTIO7	Internal oscillator block, CLKO function on RA6, port function on RA7

Fail-Safe Clock Monitor Enable bit:

FCMEN = OFF	Fail-Safe Clock Monitor disabled
FCMEN = ON	Fail-Safe Clock Monitor enabled

Internal/External Oscillator Switchover bit:

IESO = OFF	Oscillator Switchover mode disabled
IESO = ON	Oscillator Switchover mode enabled

Power-up Timer Enable bit:

PWRT = ON	PWRT enabled
PWRT = OFF	PWRT disabled

Brown-out Reset Enable bits:

BOREN = OFF	Brown-out Reset disabled in hardware and software
BOREN = ON	Brown-out Reset enabled and controlled by software (SBOREN is enabled)
BOREN = NOSLP	Brown-out Reset enabled in hardware only and disabled in Sleep mode (SBOREN is disabled)
BOREN = SBORDIS	Brown-out Reset enabled in hardware only (SBOREN is disabled)

Brown-out Reset Voltage bits:

BORV = 0	Maximum setting
BORV = 1	
BORV = 2	
BORV = 3	Minimum setting

Watchdog Timer Enable bit:

WDT = OFF	WDT disabled (control is placed on the SWDTEN bit)
WDT = ON	WDT enabled

Watchdog Timer Postscale Select bits:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Pin Enable bit:

MCLRE = OFF	RE3 input pin enabled; MCLR disabled
MCLRE = ON	MCLR pin enabled; RE3 input pin disabled

Low-Power Timer1 Oscillator Enable bit:

LPT1OSC = OFF	Timer1 configured for higher power operation
LPT1OSC = ON	Timer1 configured for low-power operation

PORTB A/D Enable bit:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input channels on Reset

CCP2 MUX bit:

CCP2MX = PORTB	CCP2 input/output is multiplexed with RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Full/Underflow Reset Enable bit:

STVREN = OFF	Stack full/underflow will not cause Reset
STVREN = ON	Stack full/underflow will cause Reset

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Extended Instruction Set Enable bit:

XINST = OFF	Instruction set extension and Indexed Addressing mode disabled (Legacy mode)
XINST = ON	Instruction set extension and Indexed Addressing mode enabled

Background Debugger Enable bit:

DEBUG = ON	Background debugger enabled, RB6 and RB7 are dedicated to In-Circuit Debug
DEBUG = OFF	Background debugger disabled, RB6 and RB7 configured as general purpose I/O pins

Code Protection Block 0:

CP0 = ON	Block 0 (000800-001FFFh) code-protected
CP0 = OFF	Block 0 (000800-001FFFh) not code-protected

Code Protection Block 1:

CP1 = ON	Block 1 (002000-003FFFh) code-protected
CP1 = OFF	Block 1 (002000-003FFFh) not code-protected

Boot Block Code Protection bit:

CPB = ON	Boot block (000000-0007FFh) code-protected
CPB = OFF	Boot block (000000-0007FFh) not code-protected

Data EEPROM Code Protection:

CPD = ON	Data EEPROM code-protected
CPD = OFF	Data EEPROM not code-protected

Write Protection Block 0:

WRT0 = ON	Block 0 (000800-001FFFh) write-protected
WRT0 = OFF	Block 0 (000800-001FFFh) not write-protected

Write Protection Block 1:

WRT1 = ON	Block 1 (002000-003FFFh) write-protected
WRT1 = OFF	Block 1 (002000-003FFFh) not write-protected

Boot Block Write Protection bit:

WRTB = ON	Boot block (000000-0007FFh) write-protected
WRTB = OFF	Boot block (000000-0007FFh) not write-protected

Configuration Register Write Protection bit:

WRTC = ON	Configuration registers (300000-3000FFh) write-protected
WRTC = OFF	Configuration registers (300000-3000FFh) not write-protected

Data EEPROM Write Protection bit:

WRTD = ON	Data EEPROM write-protected
WRTD = OFF	Data EEPROM not write-protected

Table Read Protection Block 0:

EBTR0 = ON	Block 0 (000800-001FFFh) protected from table reads executed in other blocks
EBTR0 = OFF	Block 0 (000800-001FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 1:

EBTR1 = ON	Block 1 (002000-003FFFh) protected from table reads executed in other blocks
EBTR1 = OFF	Block 1 (002000-003FFFh) not protected from table reads executed in other blocks

Boot Block Table Read Protection bit:

EBTRB = ON	Boot block (000000-0007FFFh) protected from table reads executed in other blocks
EBTRB = OFF	Boot block (000000-0007FFFh) not protected from table reads executed in other blocks

PIC18LF2523

Oscillator Selection bits:

OSC = LP	LP oscillator
OSC = XT	XT oscillator
OSC = HS	HS oscillator
OSC = RC	External RC oscillator, CLKO function on RA6
OSC = EC	EC oscillator, CLKO function on RA6
OSC = ECIO6	EC oscillator, port function on RA6
OSC = HSPLL	HS oscillator, PLL enabled (Clock Frequency = 4 x FOSC1)
OSC = RCIO6	External RC oscillator, port function on RA6
OSC = INTIO67	Internal oscillator block, port function on RA6 and RA7
OSC = INTIO7	Internal oscillator block, CLKO function on RA6, port function on RA7

Fail-Safe Clock Monitor Enable bit:

FCMEN = OFF	Fail-Safe Clock Monitor disabled
FCMEN = ON	Fail-Safe Clock Monitor enabled

Internal/External Oscillator Switchover bit:

IESO = OFF	Oscillator Switchover mode disabled
IESO = ON	Oscillator Switchover mode enabled

Power-up Timer Enable bit:

PWRT = ON	PWRT enabled
PWRT = OFF	PWRT disabled

Configuration Settings

Brown-out Reset Enable bits:

BOREN = OFF	Brown-out Reset disabled in hardware and software
BOREN = ON	Brown-out Reset enabled and controlled by software (SBOREN is enabled)
BOREN = NOSLP	Brown-out Reset enabled in hardware only and disabled in Sleep mode (SBOREN is disabled)
BOREN = SBORDIS	Brown-out Reset enabled in hardware only (SBOREN is disabled)

Brown-out Reset Voltage bits:

BORV = 0	Maximum setting
BORV = 1	
BORV = 2	
BORV = 3	Minimum setting

Watchdog Timer Enable bit:

WDT = OFF	WDT disabled (control is placed on the SWDTEN bit)
WDT = ON	WDT enabled

Watchdog Timer Postscale Select bits:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Pin Enable bit:

MCLRE = OFF	RE3 input pin enabled; MCLR disabled
MCLRE = ON	MCLR pin enabled; RE3 input pin disabled

Low-Power Timer1 Oscillator Enable bit:

LPT1OSC = OFF	Timer1 configured for higher power operation
LPT1OSC = ON	Timer1 configured for low-power operation

PORTB A/D Enable bit:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input channels on Reset

CCP2 MUX bit:

CCP2MX = PORTB	CCP2 input/output is multiplexed with RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Full/Underflow Reset Enable bit:

STVREN = OFF	Stack full/underflow will not cause Reset
STVREN = ON	Stack full/underflow will cause Reset

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Extended Instruction Set Enable bit:

XINST = OFF	Instruction set extension and Indexed Addressing mode disabled (Legacy mode)
XINST = ON	Instruction set extension and Indexed Addressing mode enabled

Background Debugger Enable bit:

DEBUG = ON	Background debugger enabled, RB6 and RB7 are dedicated to In-Circuit Debug
DEBUG = OFF	Background debugger disabled, RB6 and RB7 configured as general purpose I/O pins

Code Protection Block 0:

CP0 = ON	Block 0 (000800-001FFFh) code-protected
CP0 = OFF	Block 0 (000800-001FFFh) not code-protected

Code Protection Block 1:

CP1 = ON	Block 1 (002000-003FFFh) code-protected
CP1 = OFF	Block 1 (002000-003FFFh) not code-protected

Code Protection Block 2:

CP2 = ON	Block 2 (004000-005FFFh) code-protected
CP2 = OFF	Block 2 (004000-005FFFh) not code-protected

Code Protection Block 3:

CP3 = ON	Block 3 (006000-007FFFh) code-protected
CP3 = OFF	Block 3 (006000-007FFFh) not code-protected

Boot Block Code Protection bit:

CPB = ON	Boot block (000000-0007FFh) code-protected
CPB = OFF	Boot block (000000-0007FFh) not code-protected

Data EEPROM Code Protection:

CPD = ON	Data EEPROM code-protected
CPD = OFF	Data EEPROM not code-protected

Write Protection Block 0:

WRT0 = ON	Block 0 (000800-001FFFh) write-protected
WRT0 = OFF	Block 0 (000800-001FFFh) not write-protected

Write Protection Block 1:

WRT1 = ON	Block 1 (002000-003FFFh) write-protected
WRT1 = OFF	Block 1 (002000-003FFFh) not write-protected

Write Protection Block 2:

WRT2 = ON	Block 2 (004000-005FFFh) write-protected
WRT2 = OFF	Block 2 (004000-005FFFh) not write-protected

Write Protection Block 3:

WRT3 = ON	Block 3 (006000-007FFFh) write-protected
WRT3 = OFF	Block 3 (006000-007FFFh) not write-protected

Boot Block Write Protection bit:

WRTB = ON	Boot block (000000-0007FFFh) write-protected
WRTB = OFF	Boot block (000000-0007FFFh) not write-protected

Configuration Register Write Protection bit:

WRTC = ON	Configuration registers (300000-3000FFFh) write-protected
WRTC = OFF	Configuration registers (300000-3000FFFh) not write-protected

Data EEPROM Write Protection bit:

WRTE = ON	Data EEPROM write-protected
WRTE = OFF	Data EEPROM not write-protected

Table Read Protection Block 0:

EBTR0 = ON	Block 0 (000800-001FFFh) protected from table reads executed in other blocks
EBTR0 = OFF	Block 0 (000800-001FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 1:

EBTR1 = ON	Block 1 (002000-003FFFh) protected from table reads executed in other blocks
EBTR1 = OFF	Block 1 (002000-003FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 2:

EBTR2 = ON	Block 2 (004000-005FFFh) protected from table reads executed in other blocks
EBTR2 = OFF	Block 2 (004000-005FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 3:

EBTR3 = ON	Block 3 (006000-007FFFh) protected from table reads executed in other blocks
EBTR3 = OFF	Block 3 (006000-007FFFh) not protected from table reads executed in other blocks

Boot Block Table Read Protection bit:

EBTRB = ON	Boot block (000000-0007FFh) protected from table reads executed in other blocks
EBTRB = OFF	Boot block (000000-0007FFh) not protected from table reads executed in other blocks

PIC18LF4423

Oscillator Selection bits:

OSC = LP	LP oscillator
OSC = XT	XT oscillator
OSC = HS	HS oscillator
OSC = RC	External RC oscillator, CLKO function on RA6
OSC = EC	EC oscillator, CLKO function on RA6
OSC = ECIO6	EC oscillator, port function on RA6
OSC = HSPLL	HS oscillator, PLL enabled (Clock Frequency = 4 x FOSC1)
OSC = RCIO6	External RC oscillator, port function on RA6
OSC = INTIO67	Internal oscillator block, port function on RA6 and RA7
OSC = INTIO7	Internal oscillator block, CLKO function on RA6, port function on RA7

Fail-Safe Clock Monitor Enable bit:

FCMEN = OFF	Fail-Safe Clock Monitor disabled
FCMEN = ON	Fail-Safe Clock Monitor enabled

Internal/External Oscillator Switchover bit:

IESO = OFF	Oscillator Switchover mode disabled
IESO = ON	Oscillator Switchover mode enabled

Power-up Timer Enable bit:

PWRT = ON	PWRT enabled
PWRT = OFF	PWRT disabled

Brown-out Reset Enable bits:

BOREN = OFF	Brown-out Reset disabled in hardware and software
BOREN = ON	Brown-out Reset enabled and controlled by software (SBOREN is enabled)
BOREN = NOSLP	Brown-out Reset enabled in hardware only and disabled in Sleep mode (SBOREN is disabled)
BOREN = SBORDIS	Brown-out Reset enabled in hardware only (SBOREN is disabled)

Brown-out Reset Voltage bits:

BORV = 0	Maximum setting
BORV = 1	
BORV = 2	
BORV = 3	Minimum setting

Watchdog Timer Enable bit:

WDT = OFF	WDT disabled (control is placed on the SWDTEN bit)
WDT = ON	WDT enabled

Watchdog Timer Postscale Select bits:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Pin Enable bit:

MCLRE = OFF	RE3 input pin enabled; MCLR disabled
MCLRE = ON	MCLR pin enabled; RE3 input pin disabled

Low-Power Timer1 Oscillator Enable bit:

LPT1OSC = OFF	Timer1 configured for higher power operation
LPT1OSC = ON	Timer1 configured for low-power operation

PORTB A/D Enable bit:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input channels on Reset

CCP2 MUX bit:

CCP2MX = PORTB	CCP2 input/output is multiplexed with RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Full/Underflow Reset Enable bit:

STVREN = OFF	Stack full/underflow will not cause Reset
STVREN = ON	Stack full/underflow will cause Reset

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Extended Instruction Set Enable bit:

XINST = OFF	Instruction set extension and Indexed Addressing mode disabled (Legacy mode)
XINST = ON	Instruction set extension and Indexed Addressing mode enabled

Background Debugger Enable bit:

DEBUG = ON	Background debugger enabled, RB6 and RB7 are dedicated to In-Circuit Debug
DEBUG = OFF	Background debugger disabled, RB6 and RB7 configured as general purpose I/O pins

Code Protection Block 0:

CP0 = ON	Block 0 (000800-001FFFh) code-protected
CP0 = OFF	Block 0 (000800-001FFFh) not code-protected

Code Protection Block 1:

CP1 = ON	Block 1 (002000-003FFFh) code-protected
CP1 = OFF	Block 1 (002000-003FFFh) not code-protected

Boot Block Code Protection bit:

CPB = ON	Boot block (000000-0007FFh) code-protected
CPB = OFF	Boot block (000000-0007FFh) not code-protected

Data EEPROM Code Protection:

CPD = ON	Data EEPROM code-protected
CPD = OFF	Data EEPROM not code-protected

Write Protection Block 0:

WRT0 = ON	Block 0 (000800-001FFFh) write-protected
WRT0 = OFF	Block 0 (000800-001FFFh) not write-protected

Write Protection Block 1:

WRT1 = ON	Block 1 (002000-003FFFh) write-protected
WRT1 = OFF	Block 1 (002000-003FFFh) not write-protected

Boot Block Write Protection bit:

WRTB = ON	Boot block (000000-0007FFh) write-protected
WRTB = OFF	Boot block (000000-0007FFh) not write-protected

Configuration Register Write Protection bit:

WRTC = ON	Configuration registers (300000-3000FFh) write-protected
WRTC = OFF	Configuration registers (300000-3000FFh) not write-protected

Data EEPROM Write Protection bit:

WRTD = ON	Data EEPROM write-protected
WRTD = OFF	Data EEPROM not write-protected

Table Read Protection Block 0:

EBTR0 = ON	Block 0 (000800-001FFFh) protected from table reads executed in other blocks
EBTR0 = OFF	Block 0 (000800-001FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 1:

EBTR1 = ON	Block 1 (002000-003FFFh) protected from table reads executed in other blocks
EBTR1 = OFF	Block 1 (002000-003FFFh) not protected from table reads executed in other blocks

Boot Block Table Read Protection bit:

EBTRB = ON	Boot block (000000-0007FFFh) protected from table reads executed in other blocks
EBTRB = OFF	Boot block (000000-0007FFFh) not protected from table reads executed in other blocks

PIC18LF4523

Oscillator Selection bits:

OSC = LP	LP oscillator
OSC = XT	XT oscillator
OSC = HS	HS oscillator
OSC = RC	External RC oscillator, CLKO function on RA6
OSC = EC	EC oscillator, CLKO function on RA6
OSC = ECIO6	EC oscillator, port function on RA6
OSC = HSPLL	HS oscillator, PLL enabled (Clock Frequency = 4 x FOSC1)
OSC = RCIO6	External RC oscillator, port function on RA6
OSC = INTIO67	Internal oscillator block, port function on RA6 and RA7
OSC = INTIO7	Internal oscillator block, CLKO function on RA6, port function on RA7

Fail-Safe Clock Monitor Enable bit:

FCMEN = OFF	Fail-Safe Clock Monitor disabled
FCMEN = ON	Fail-Safe Clock Monitor enabled

Internal/External Oscillator Switchover bit:

IESO = OFF	Oscillator Switchover mode disabled
IESO = ON	Oscillator Switchover mode enabled

Power-up Timer Enable bit:

PWRT = ON	PWRT enabled
PWRT = OFF	PWRT disabled

Configuration Settings

Brown-out Reset Enable bits:

BOREN = OFF	Brown-out Reset disabled in hardware and software
BOREN = ON	Brown-out Reset enabled and controlled by software (SBOREN is enabled)
BOREN = NOSLP	Brown-out Reset enabled in hardware only and disabled in Sleep mode (SBOREN is disabled)
BOREN = SBORDIS	Brown-out Reset enabled in hardware only (SBOREN is disabled)

Brown-out Reset Voltage bits:

BORV = 0	Maximum setting
BORV = 1	
BORV = 2	
BORV = 3	Minimum setting

Watchdog Timer Enable bit:

WDT = OFF	WDT disabled (control is placed on the SWDTEN bit)
WDT = ON	WDT enabled

Watchdog Timer Postscale Select bits:

WDTPS = 1	1:1
WDTPS = 2	1:2
WDTPS = 4	1:4
WDTPS = 8	1:8
WDTPS = 16	1:16
WDTPS = 32	1:32
WDTPS = 64	1:64
WDTPS = 128	1:128
WDTPS = 256	1:256
WDTPS = 512	1:512
WDTPS = 1024	1:1024
WDTPS = 2048	1:2048
WDTPS = 4096	1:4096
WDTPS = 8192	1:8192
WDTPS = 16384	1:16384
WDTPS = 32768	1:32768

MCLR Pin Enable bit:

MCLRE = OFF	RE3 input pin enabled; MCLR disabled
MCLRE = ON	MCLR pin enabled; RE3 input pin disabled

Low-Power Timer1 Oscillator Enable bit:

LPT1OSC = OFF	Timer1 configured for higher power operation
LPT1OSC = ON	Timer1 configured for low-power operation

PORTB A/D Enable bit:

PBADEN = OFF	PORTB<4:0> pins are configured as digital I/O on Reset
PBADEN = ON	PORTB<4:0> pins are configured as analog input channels on Reset

CCP2 MUX bit:

CCP2MX = PORTB	CCP2 input/output is multiplexed with RB3
CCP2MX = PORTC	CCP2 input/output is multiplexed with RC1

Stack Full/Underflow Reset Enable bit:

STVREN = OFF	Stack full/underflow will not cause Reset
STVREN = ON	Stack full/underflow will cause Reset

Low Voltage ICSP:

LVP = OFF	Disabled
LVP = ON	Enabled

Extended Instruction Set Enable bit:

XINST = OFF	Instruction set extension and Indexed Addressing mode disabled (Legacy mode)
XINST = ON	Instruction set extension and Indexed Addressing mode enabled

Background Debugger Enable bit:

DEBUG = ON	Background debugger enabled, RB6 and RB7 are dedicated to In-Circuit Debug
DEBUG = OFF	Background debugger disabled, RB6 and RB7 configured as general purpose I/O pins

Code Protection Block 0:

CP0 = ON	Block 0 (000800-001FFFh) code-protected
CP0 = OFF	Block 0 (000800-001FFFh) not code-protected

Code Protection Block 1:

CP1 = ON	Block 1 (002000-003FFFh) code-protected
CP1 = OFF	Block 1 (002000-003FFFh) not code-protected

Code Protection Block 2:

CP2 = ON	Block 2 (004000-005FFFh) code-protected
CP2 = OFF	Block 2 (004000-005FFFh) not code-protected

Code Protection Block 3:

CP3 = ON	Block 3 (006000-007FFFh) code-protected
CP3 = OFF	Block 3 (006000-007FFFh) not code-protected

Boot Block Code Protection bit:

CPB = ON	Boot block (000000-0007FFh) code-protected
CPB = OFF	Boot block (000000-0007FFh) not code-protected

Data EEPROM Code Protection:

CPD = ON	Data EEPROM code-protected
CPD = OFF	Data EEPROM not code-protected

Write Protection Block 0:

WRT0 = ON	Block 0 (000800-001FFFh) write-protected
WRT0 = OFF	Block 0 (000800-001FFFh) not write-protected

Write Protection Block 1:

WRT1 = ON	Block 1 (002000-003FFFh) write-protected
WRT1 = OFF	Block 1 (002000-003FFFh) not write-protected

Write Protection Block 2:

WRT2 = ON	Block 2 (004000-005FFFh) write-protected
WRT2 = OFF	Block 2 (004000-005FFFh) not write-protected

Write Protection Block 3:

WRT3 = ON	Block 3 (006000-007FFFh) write-protected
WRT3 = OFF	Block 3 (006000-007FFFh) not write-protected

Boot Block Write Protection bit:

WRTB = ON	Boot block (000000-0007FFFh) write-protected
WRTB = OFF	Boot block (000000-0007FFFh) not write-protected

Configuration Register Write Protection bit:

WRTC = ON	Configuration registers (300000-3000FFFh) write-protected
WRTC = OFF	Configuration registers (300000-3000FFFh) not write-protected

Data EEPROM Write Protection bit:

WRTE = ON	Data EEPROM write-protected
WRTE = OFF	Data EEPROM not write-protected

Table Read Protection Block 0:

EBTR0 = ON	Block 0 (000800-001FFFh) protected from table reads executed in other blocks
EBTR0 = OFF	Block 0 (000800-001FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 1:

EBTR1 = ON	Block 1 (002000-003FFFh) protected from table reads executed in other blocks
EBTR1 = OFF	Block 1 (002000-003FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 2:

EBTR2 = ON	Block 2 (004000-005FFFh) protected from table reads executed in other blocks
EBTR2 = OFF	Block 2 (004000-005FFFh) not protected from table reads executed in other blocks

Table Read Protection Block 3:

EBTR3 = ON	Block 3 (006000-007FFFh) protected from table reads executed in other blocks
EBTR3 = OFF	Block 3 (006000-007FFFh) not protected from table reads executed in other blocks

Boot Block Table Read Protection bit:

EBTRB = ON	Boot block (000000-0007FFh) protected from table reads executed in other blocks
EBTRB = OFF	Boot block (000000-0007FFh) not protected from table reads executed in other blocks



WORLDWIDE SALES AND SERVICE

AMERICAS

Corporate Office

2355 West Chandler Blvd.
Chandler, AZ 85224-6199
Tel: 480-792-7200
Fax: 480-792-7277
Technical Support:
<http://support.microchip.com>
Web Address:
www.microchip.com

Atlanta

Alpharetta, GA
Tel: 770-640-0034
Fax: 770-640-0307

Boston

Westborough, MA
Tel: 774-760-0087
Fax: 774-760-0088

Chicago

Itasca, IL
Tel: 630-285-0071
Fax: 630-285-0075

Dallas

Addison, TX
Tel: 972-818-7423
Fax: 972-818-2924

Detroit

Farmington Hills, MI
Tel: 248-538-2250
Fax: 248-538-2260

Kokomo

Kokomo, IN
Tel: 765-864-8360
Fax: 765-864-8387

Los Angeles

Mission Viejo, CA
Tel: 949-462-9523
Fax: 949-462-9608

San Jose

Mountain View, CA
Tel: 650-215-1444
Fax: 650-961-0286

Toronto

Mississauga, Ontario,
Canada
Tel: 905-673-0699
Fax: 905-673-6509

ASIA/PACIFIC

Australia - Sydney

Tel: 61-2-9868-6733
Fax: 61-2-9868-6755

China - Beijing

Tel: 86-10-8528-2100
Fax: 86-10-8528-2104

China - Chengdu

Tel: 86-28-8676-6200
Fax: 86-28-8676-6599

China - Fuzhou

Tel: 86-591-8750-3506
Fax: 86-591-8750-3521

China - Hong Kong SAR

Tel: 852-2401-1200
Fax: 852-2401-3431

China - Qingdao

Tel: 86-532-8502-7355
Fax: 86-532-8502-7205

China - Shanghai

Tel: 86-21-5407-5533
Fax: 86-21-5407-5066

China - Shenyang

Tel: 86-24-2334-2829
Fax: 86-24-2334-2393

China - Shenzhen

Tel: 86-755-8203-2660
Fax: 86-755-8203-1760

China - Shunde

Tel: 86-757-2839-5507
Fax: 86-757-2839-5571

China - Wuhan

Tel: 86-27-5980-5300
Fax: 86-27-5980-5118

China - Xian

Tel: 86-29-8833-7250
Fax: 86-29-8833-7256

ASIA/PACIFIC

India - Bangalore

Tel: 91-80-2229-0061
Fax: 91-80-2229-0062

India - New Delhi

Tel: 91-11-5160-8631
Fax: 91-11-5160-8632

India - Pune

Tel: 91-20-2566-1512
Fax: 91-20-2566-1513

Japan - Yokohama

Tel: 81-45-471-6166
Fax: 81-45-471-6122

Korea - Gumi

Tel: 82-54-473-4301
Fax: 82-54-473-4302

Korea - Seoul

Tel: 82-2-554-7200
Fax: 82-2-558-5932 or
82-2-558-5934

Malaysia - Penang

Tel: 604-646-8870
Fax: 604-646-5086

Philippines - Manila

Tel: 632-634-9065
Fax: 632-634-9069

Singapore

Tel: 65-6334-8870
Fax: 65-6334-8850

Taiwan - Hsin Chu

Tel: 886-3-572-9526
Fax: 886-3-572-6459

Taiwan - Kaohsiung

Tel: 886-7-536-4818
Fax: 886-7-536-4803

Taiwan - Taipei

Tel: 886-2-2500-6610
Fax: 886-2-2508-0102

Thailand - Bangkok

Tel: 66-2-694-1351
Fax: 66-2-694-1350

EUROPE

Austria - Weis

Tel: 43-7242-2244-399
Fax: 43-7242-2244-393

Denmark - Copenhagen

Tel: 45-4450-2828
Fax: 45-4485-2829

France - Paris

Tel: 33-1-69-53-63-20
Fax: 33-1-69-30-90-79

Germany - Munich

Tel: 49-89-627-144-0
Fax: 49-89-627-144-44

Italy - Milan

Tel: 39-0331-742611
Fax: 39-0331-466781

Netherlands - Drunen

Tel: 31-416-690399
Fax: 31-416-690340

Spain - Madrid

Tel: 34-91-352-30-52
Fax: 34-91-352-11-47

UK - Wokingham

Tel: 44-118-921-5869
Fax: 44-118-921-5820