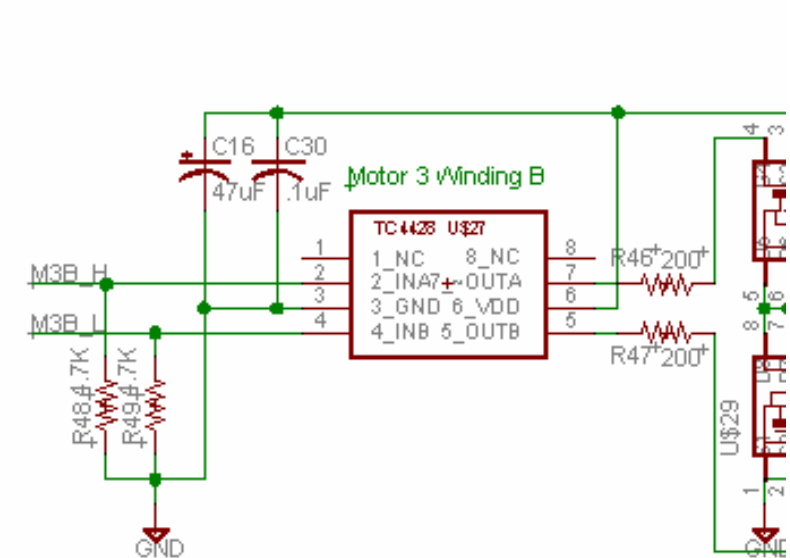
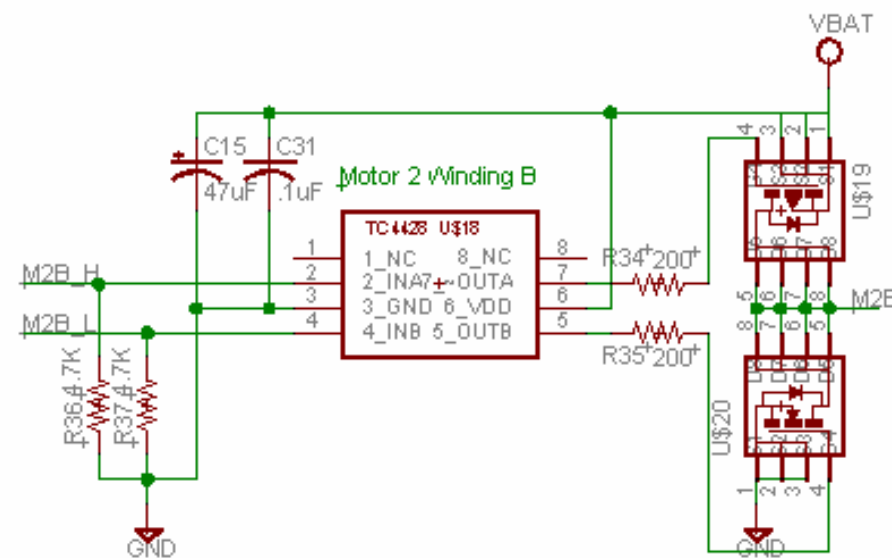
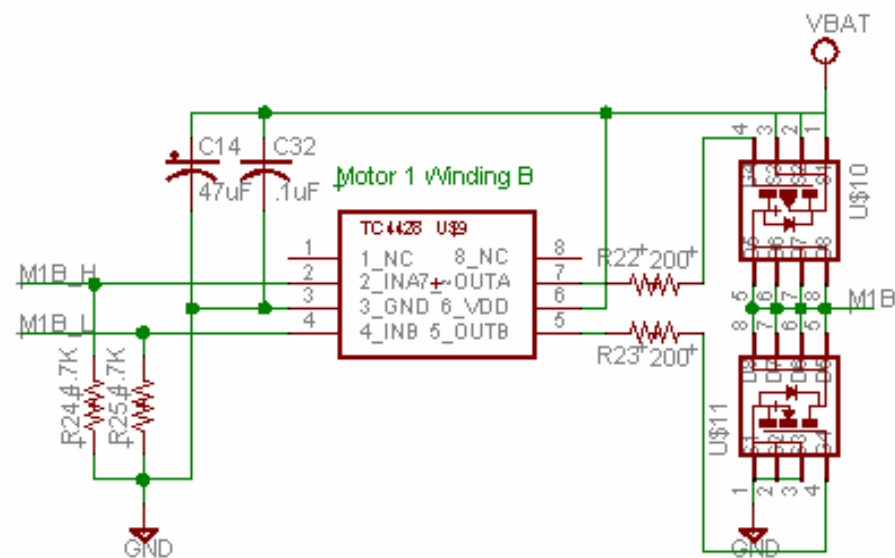
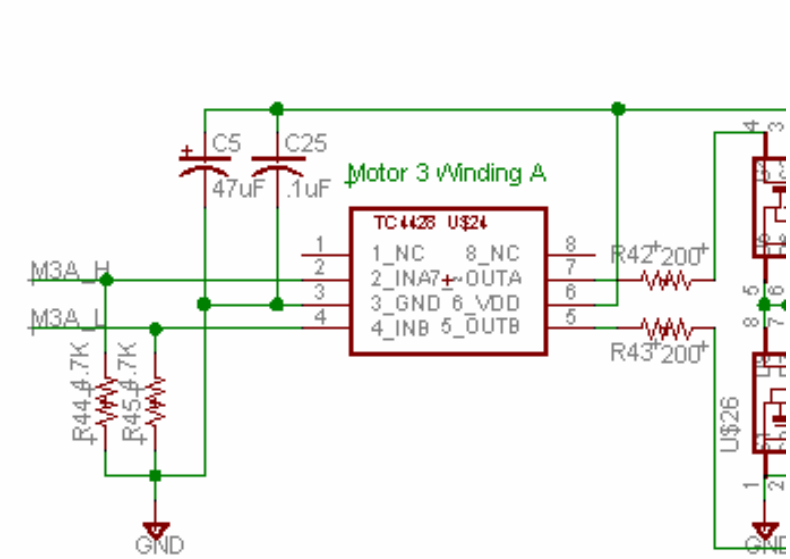
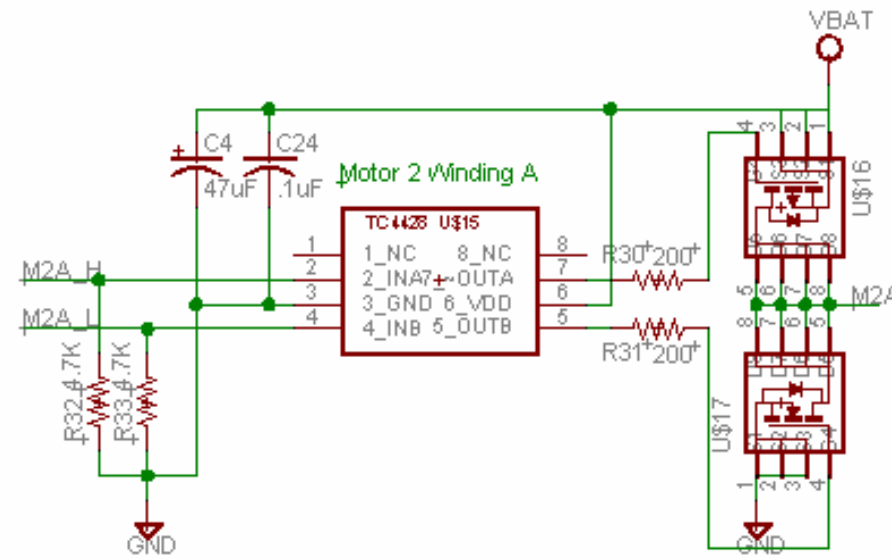
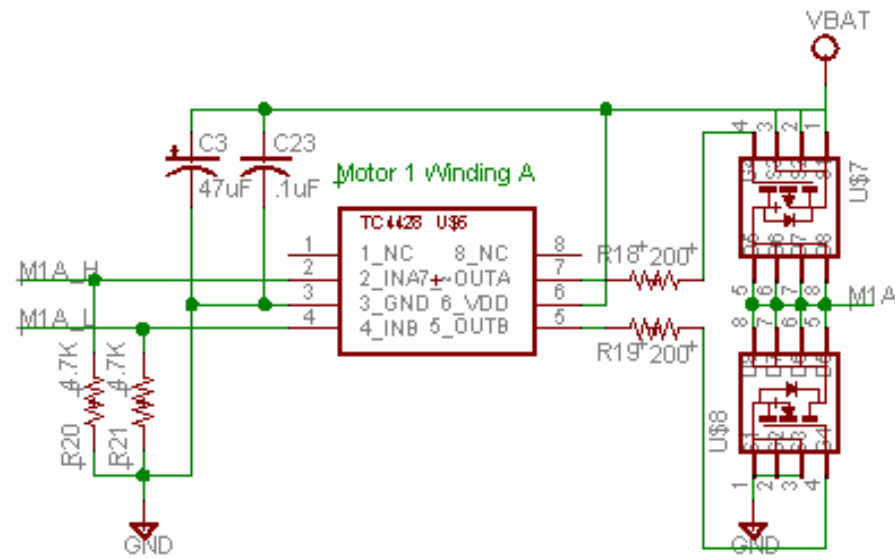
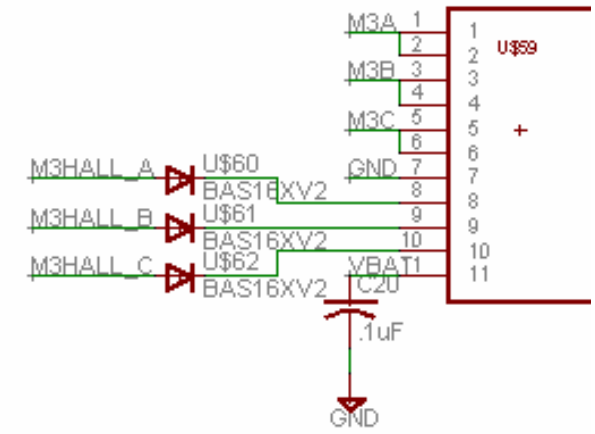
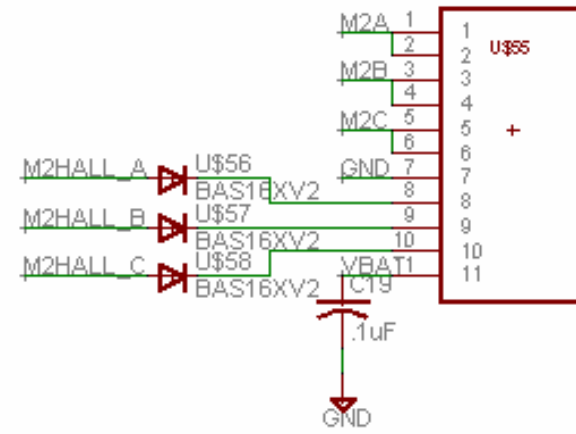
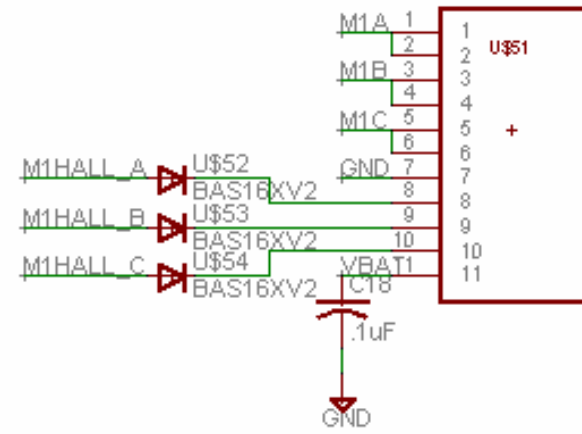
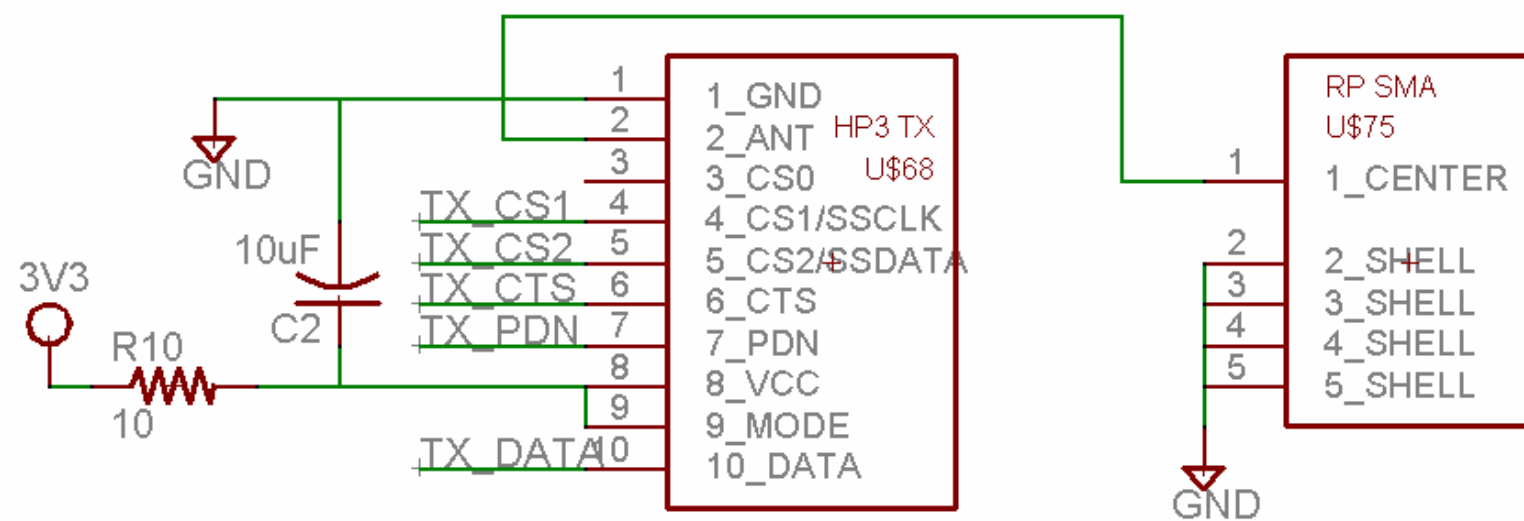
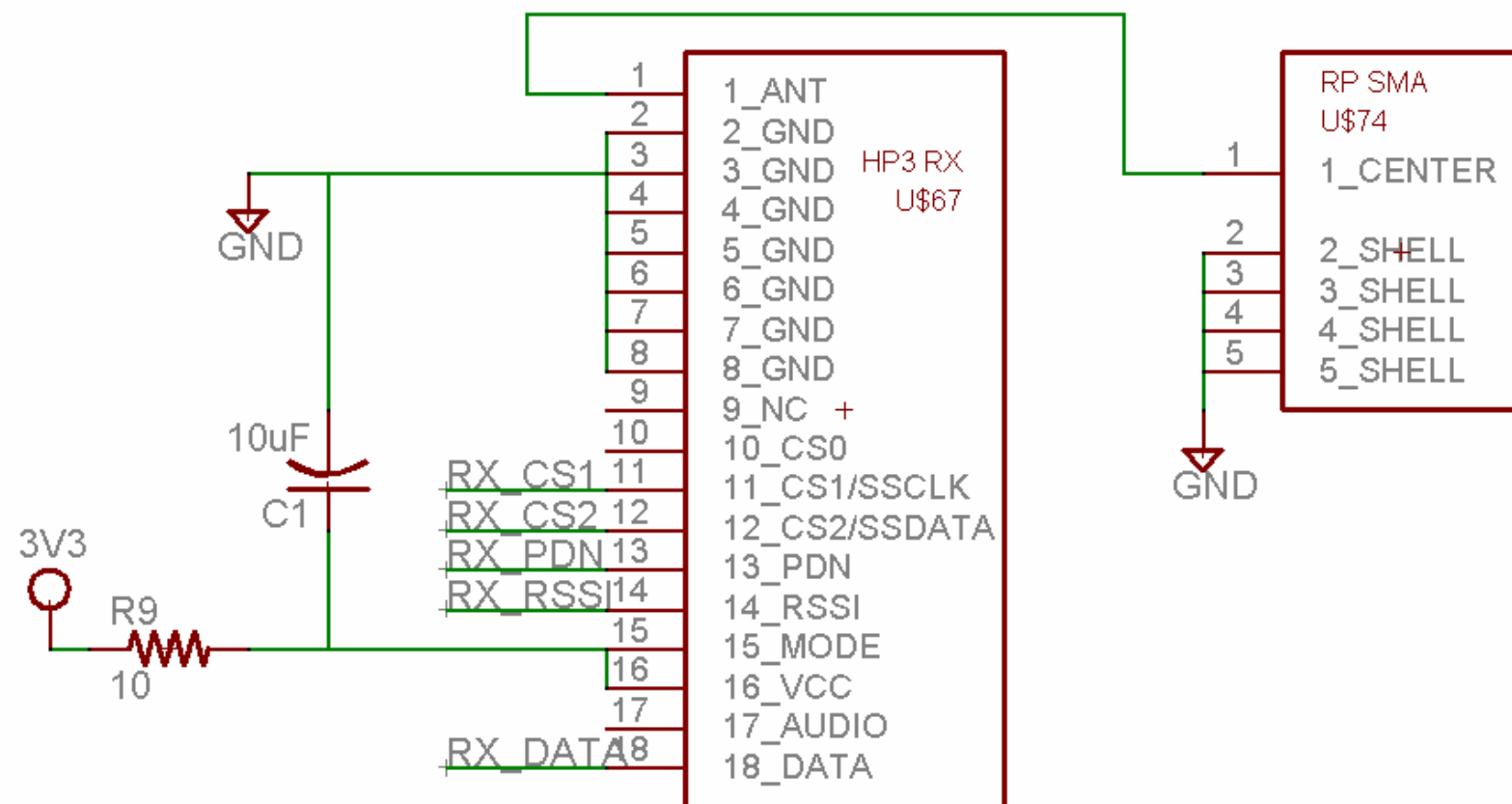
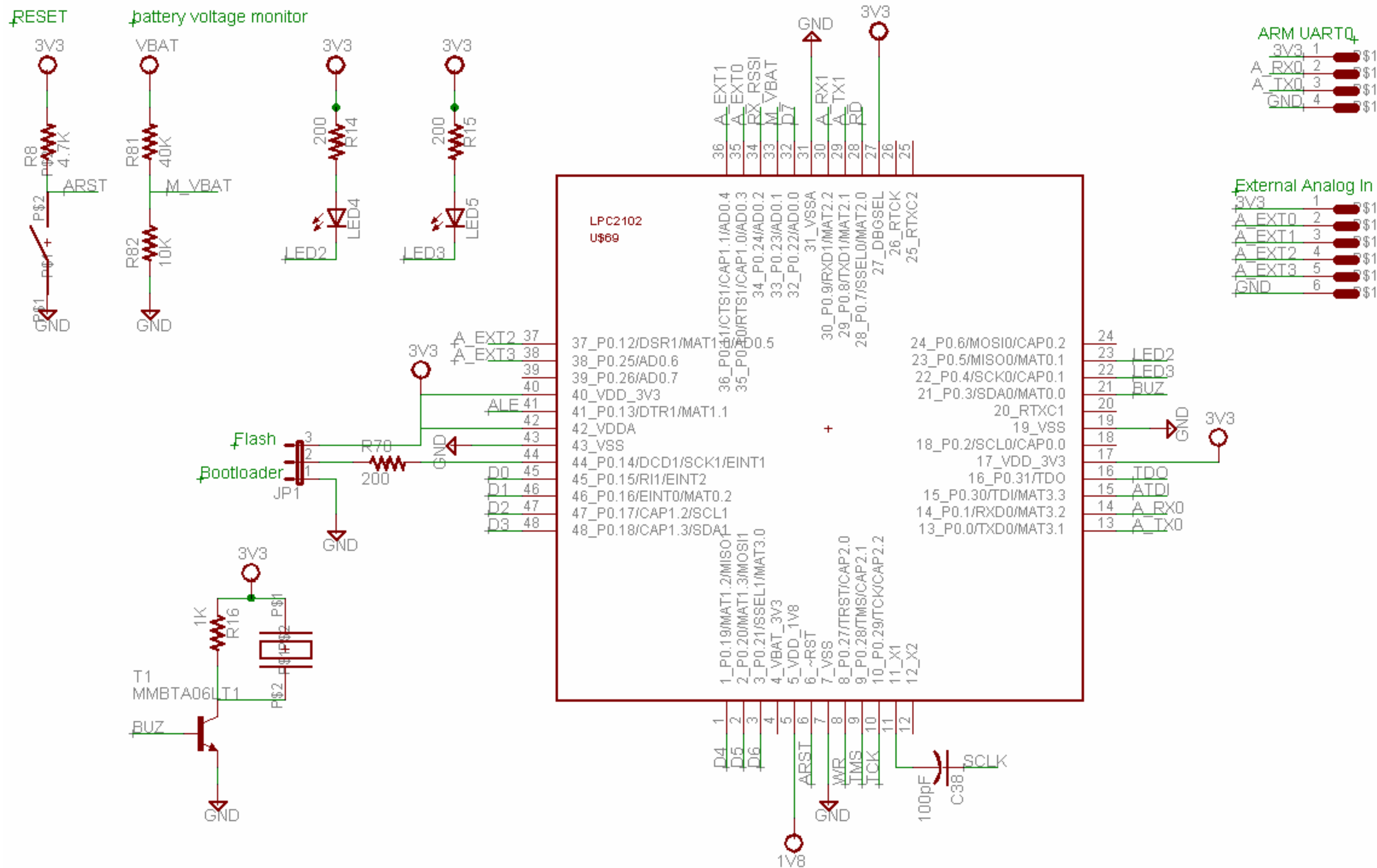


Sheet 2 Detail







ARM UART

3V3	1	5\$1
A_RX0	2	5\$1
A_TX0	3	5\$1
GND	4	5\$1

External Analog In

3V3	1	5\$1
A_EXT0	2	5\$1
A_EXT1	3	5\$1
A_EXT2	4	5\$1
A_EXT3	5	5\$1
GND	6	5\$1

